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IPv4-Embedded IPv6 Multicast Address Format
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Abstract

This document specifies an extension to the IPv6 multicast addressing architecture to be used in the context of IPv4-IPv6 interconnection. In particular, this document defines an address format for IPv4-embedded IPv6 multicast addresses. This address format can be used for IPv4-IPv6 translation or encapsulation schemes.

This document updates [RFC4291](#).

Requirements Language

The key words "MUST", "MUST NOT", "REQUIRED", "SHALL", "SHALL NOT", "SHOULD", "SHOULD NOT", "RECOMMENDED", "MAY", and "OPTIONAL" in this document are to be interpreted as described in [RFC 2119](#) [[RFC2119](#)].

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64 Multicast Address Format

January 2012

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1. Introduction

This document specifies an extension to the IPv6 multicast addressing architecture to be used in the context of IPv4-IPv6 interconnection. In particular, this document defines an address format for IPv4-embedded IPv6 multicast addresses. This address format can be used for IPv4-IPv6 translation or encapsulation schemes.

This document updates [[RFC4291](#)].

This specification can be used in conjunction with other extensions such as building unicast prefix-based multicast IPv6 address [[RFC3306](#)] or embedding the rendezvous point [[RFC3956](#)].

This document is a companion document to [[RFC6052](#)] which focuses exclusively on IPv4-embedded IPv6 unicast addresses.

Details about design choices are documented in [Appendix A](#).

2. Terminology

This document makes use of the following terms:

- o IPv4-embedded IPv6 multicast address: denotes a multicast IPv6 address which includes in 32 bits an IPv4 address. Two types of IPv6 addresses are defined that carry an IPv4 address in the low-order 32 bits of the address. The format to build such addresses is defined in [Section 3](#) for ASM mode and [Section 4](#) for SSM mode.
- o Multicast Prefix64 (or MPREFIX64 for short) refers to an IPv6

multicast prefix to be used to construct IPv4-embedded IPv6 multicast addresses.

- o ASM_MPREFIX64: denotes a multicast Prefix64 used in ASM mode. It follows the format described in [Section 3](#).
- o SSM_MPREFIX64: denotes a multicast Prefix64 used in SSM mode. It follows the format described in [Section 4](#).

3. IPv4-Embedded IPv6 Multicast Address Format: ASM Mode

To meet the requirements listed in [Appendix A.2](#), the following address format is defined to enclose an IPv4 multicast address when ASM mode is used:

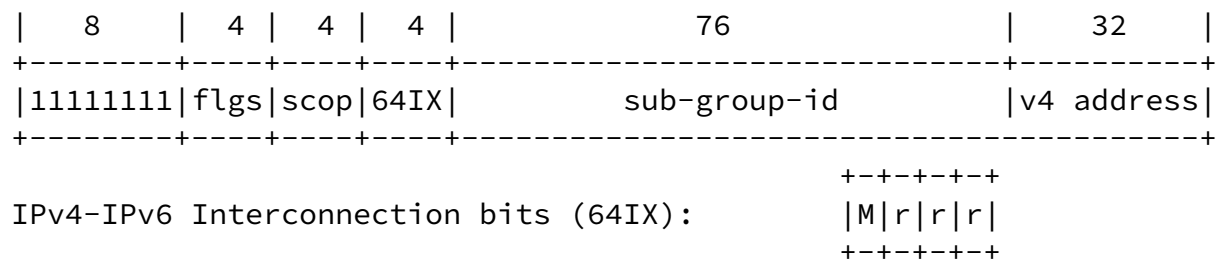


Figure 1: IPv4-Embedded IPv6 Multicast Address Format: ASM Mode

The description of the fields is as follows:

- o "flgs" and "scop" fields are defined in [\[RFC4291\]](#).
- o 64IX field (IPv4-IPv6 interconnection bits): The first bit is the M-bit. When "M-bit" is set to 1, it indicates that an multicast IPv4 address is embedded in the low-order 32 bits of the multicast IPv6 address. All the remaining bits are reserved and MUST be set to 0.
- o sub-group-id: This field is configurable according to local policies of the entity managing the IPv4-IPv6 Interconnection Function. This field must follow the recommendations specified in [\[RFC3306\]](#) if unicast-based prefix is used or the recommendations specified in [\[RFC3956\]](#) if embedded-RP is used. The default value

- is all zeros.
- o The low-order 32 bits MUST include an IPv4 multicast address when the M-bit is set to 1. The enclosed IPv4 multicast address SHOULD NOT be in 232/8 range.

4. IPv4-Embedded IPv6 Multicast Address Format: SSM Mode

As mentioned above, any IPv4-embedded IPv6 address used in SSM mode MUST be part of ff3x::/32 [[RFC4607](#)]. Figure 2 describes the format of the IPv6 multicast address to be used to enclose an IPv4 multicast address.

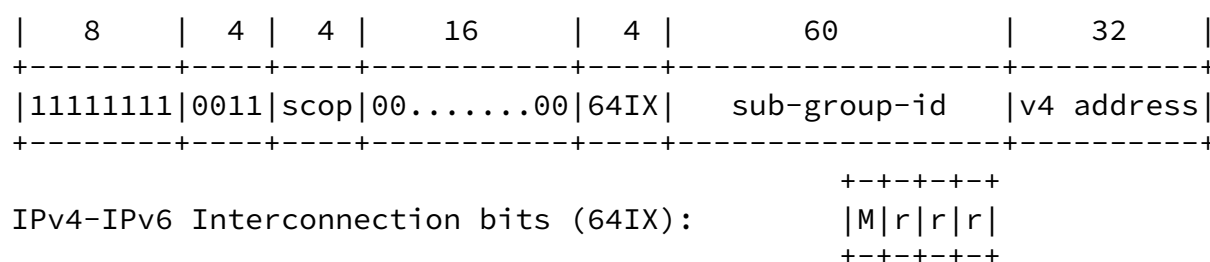


Figure 2: IPv4-Embedded IPv6 Multicast Address Format: SSM Mode

The description of the fields is as follows:

- o Flags must be set to 0011.
- o "scop" is defined in [[RFC4291](#)].
- o 64IX field (IPv4-IPv6 interconnection bits): Same meaning as

- o sub-group-id: The default value is all zeros.
- o The low-order 32 bits MUST include an IPv4 multicast address when the M-bit is set to 1. The embedded IPv4 address SHOULD be in the 232/8 range [[RFC4607](#)]. 232.0.0.1-232.0.0.255 range is being reserved to IANA.

The embedded IPv4 address in an IPv6 multicast address is included in the last 32 bits; therefore dotted decimal notation can be used.

The format of the MPREFIX64 should be compatible with what is specified in [RFC3306] and [RFC3956] if corresponding mechanisms are used.

8	4	4	4	76	32
11111111	flgs	scop	64IX	sub-group-id	v4 address
v				v	v

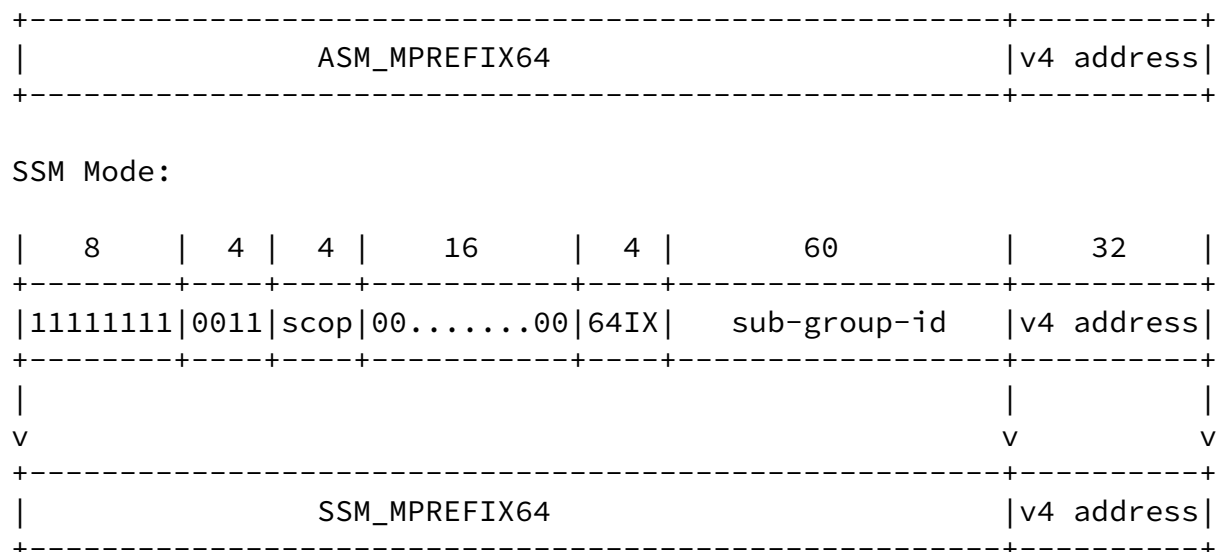


Figure 3: MPREFIX64

7. Source IPv4 Address in the IPv6 Ream

An IPv4 source is represented in the IPv6 realm with its IPv4-converted IPv6 address [[RFC6052](#)].

8. IANA Considerations

Authors of this document request to reserve:

- o ff3x:0:8000/33 SSM block to embed an IPv4 multicast address in the last 32 bits.
- o ffx:8000/17 ASM block to embed an IPv4 multicast address in the last 32 bits.

9. Security Considerations

This document defined an address format to embed an IPv4 multicast address in an IPv6 multicast address. The same security considerations as those discussed in [[RFC6052](#)] are to be taken into

[10.](#) Acknowledgements

Many thanks to R. Bonica, B. Sarikaya, P. Savola and T. Tsou for their comments and review.

[11.](#) Normative References

- [RFC2119] Bradner, S., "Key words for use in RFCs to Indicate Requirement Levels", [BCP 14](#), [RFC 2119](#), March 1997.
- [RFC3306] Haberman, B. and D. Thaler, "Unicast-Prefix-based IPv6 Multicast Addresses", [RFC 3306](#), August 2002.
- [RFC3956] Savola, P. and B. Haberman, "Embedding the Rendezvous Point (RP) Address in an IPv6 Multicast Address", [RFC 3956](#), November 2004.
- [RFC4291] Hinden, R. and S. Deering, "IP Version 6 Addressing Architecture", [RFC 4291](#), February 2006.
- [RFC4607] Holbrook, H. and B. Cain, "Source-Specific Multicast for IP", [RFC 4607](#), August 2006.
- [RFC6052] Bao, C., Huitema, C., Bagnulo, M., Boucadair, M., and X. Li, "IPv6 Addressing of IPv4/IPv6 Translators", [RFC 6052](#), October 2010.

[Appendix A.](#) Design Choices

[A.1.](#) Location of the IPv4 Address

There is no strong argument to allow for flexible options to encode the IPv4 address inside the multicast IPv6 address. The option retained by the authors is to encode the multicast IPv4 address in the low-order 32 bits of the IPv6 address.

This choice is also motivated by the need to be compliant with [\[RFC3306\]](#) and [\[RFC3956\]](#).

[A.2.](#) Location of the M-bit

Figure 4 is a reminder of the IPv6 multicast address format as defined in [\[RFC4291\]](#):

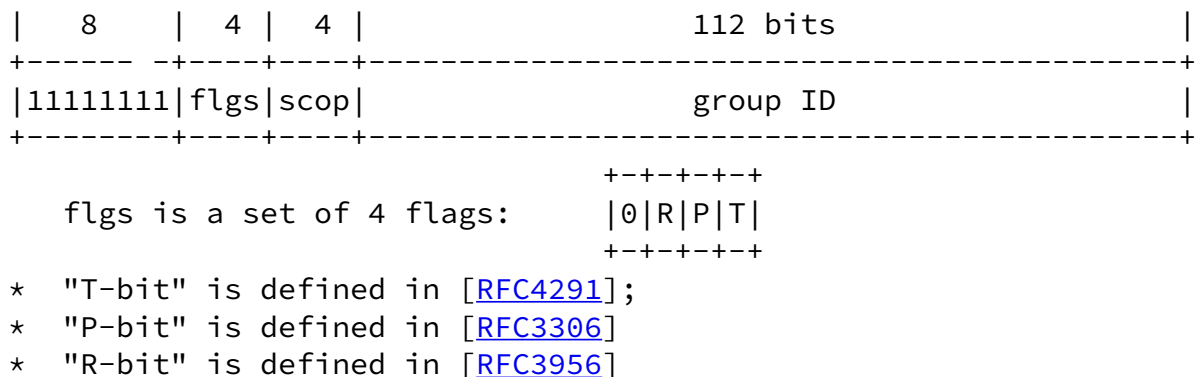


Figure 4: IPv6 Multicast address format as defined in [RFC4291](#)

It was tempting to use the remaining flag to indicate whether an IPv6 address embeds an IPv4 address or not. This choice has been abandoned by the authors for various reasons:

- o ff3x::/32 is defined as SSM. Defining a new flag would require standards and implementations to also treat ffbx::/32 as SSM.
- o Prefixes starting with ff7x are defined as embedded-RP, but not prefixes starting with fffx. Blow is provided an excerpt from [\[RFC3956\]](#):

" ...the encoding and the protocol mode used when the two high-order bits in "flgs" are set to 11 ("fff0::/12") is intentionally unspecified until such time that the highest-order bit is defined. Without further IETF specification, implementations SHOULD NOT treat the fff0::/12 range as Embedded-RP."

as such defining a new flag would require implementations to also treat ff7x::/12 as embedded-RP prefix.
- o This is the last remaining flag and at this stage we are not sure whether there is other usage scenarios of the flag.

As a conclusion, the remaining flag is not used to indicate an IPv6 multicast address embeds an IPv4 multicast address. However the following constraints should be met:

- (1) Belong to ff3x::/32 and be compatible with unicast-based prefix [\[RFC3306\]](#) for SSM. Note that [\[RFC3306\]](#) suggests to set "plen" to 0 and "network-prefix" to 0.
- (2) Be compatible with embedded-RP [\[RFC3956\]](#) and unicast-based prefix [\[RFC3306\]](#) for ASM;
- (3) Avoid ff3x::4000:0001-ff3x::7fff:ffff which is reserved for IANA.

Meeting (1) and (2) with the same location of the M-bit is not feasible without modifying embedded-RP and unicast-based prefix specifications; this option is avoided.

As a consequence, two multicast blocks are proposed to be used when

embedding IPv4 address: one block for ASM ([Section 3](#)) and another one for the SSM ([Section 4](#)).

[A.3](#). Encapsulation vs. Translation

IPv4-IPv6 encapsulator and translator may be embedded in the same device or even implemented with the same software module. In order to help the function whether an encapsulated IPv6 multicast packets or translated IPv6 ones are to be transferred. It was tempting to define an S-bit for that purpose but this choice has been abandoned in favor of the recommendation to use distinct MPREFIX64 for each scheme.

As such, there is no need to reserve a bit in the IPv6 multicast address to separate between the translation and the encapsulation schemes.

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