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Distribution of TRILL Link-State using BGP
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Abstract

This draft describes a TRILL link state and MAC address reachability information distribution mechanism using a BGP LS extension. External components such as an SDN Controller can use the information for topology visibility, troubleshooting, network automation, and the like. This document updates [RFC 7752](#).

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BGP LS For TRILL

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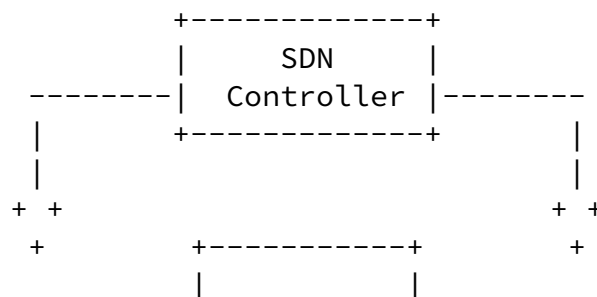
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1. Introduction

BGP has been extended to distribute IGP link-state and traffic engineering information to some external components [RFC7752], such as the PCE and ALTO servers. The information can be used by these external components to compute a MPLS-TE path across IGP areas, visualize and abstract network topology, and the like.

TRILL (Transparent Interconnection of Lots of Links) protocol [\[RFC6325\]](#) [\[RFC7780\]](#) provides a solution for least cost transparent routing in multi-hop networks with arbitrary topologies and link technologies, using [\[IS-IS\]](#) [\[RFC7176\]](#) link-state routing and a hop count. TRILL switches are sometimes called RBridges (Routing Bridges).

The TRILL protocol has been deployed in many data center networks. Data center automation is a vital step to increase the speed and agility of business. An SDN controller as an external component normally can be used to provide centralized control and automation for the data center network. Providing a holistic view of whole network topology to the SDN controller is an important part of data center network automation and troubleshooting.



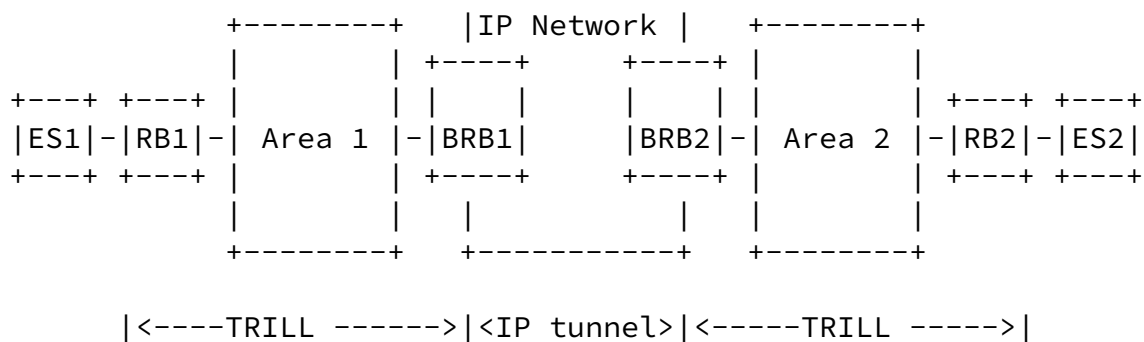


Figure 1: TRILL interconnection

In Data Center interconnection scenario illustrated in Figure 1, a single SDN Controller or network management system (NMS) can be used for end-to-end network management. End-to-end topology visibility on the SDN controller or NMS is very useful for whole network automation and troubleshooting. BGP LS can be used by the external SDN controller to collect multiple TRILL domain's link-state.

BGP LS also can be used for MAC address reachability information synchronization across multiple TRILL domains. The transported MAC reachability information and the like is for telemetry purposes and for use by SDN controller(s) where the coordination or protocol between the SDN controllers is out of scope.

This document describes the detailed BGP LS extension mechanisms for TRILL link state and MAC address reachability information distribution. This document updated [\[RFC7752\]](#) by creating a new IANA registry for BGP-LS Node Descriptor Flag Bits.

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BGP LS For TRILL

[2.](#) Conventions used in this document

The key words "MUST", "MUST NOT", "REQUIRED", "SHALL", "SHALL NOT", "SHOULD", "SHOULD NOT", "RECOMMENDED", "NOT RECOMMENDED", "MAY", and "OPTIONAL" in this document are to be interpreted as described in [BCP 14](#) [[RFC2119](#)] [[RFC8174](#)] when, and only when, they appear in all capitals, as shown here.

BGP - Border Gateway Protocol

BGP-LS - BGP Link-State

Data label - VLAN or FGL (Fine Grained Label [[RFC7172](#)])

IGP - Interior Gateway Protocol

IS - Intermediate System (for this document, all relevant intermediate systems are RBridges)

LS - Link State

NLRI - Network Layer Reachability Information

SDN - Software Defined Networking

RBridge - A device implementing the TRILL protocol

TRILL - Transparent Interconnection of Lots of Links [[RFC6325](#)]
[[RFC7176](#)] [[RFC7780](#)]

[3.](#) Carrying TRILL Link-State Information in BGP

In [[RFC7752](#)], several BGP-LS NLRI types are defined. For TRILL link-state distribution, the Node NLRI and Link NLRI are extended to carry layer 3 gateway role and link MTU information. TRILL specific attributes are carried using opaque Node Attribute TLVs. Examples of such attributes are nickname, distribution tree number and

identifiers, interested VLANs/Fine Grained Label, and multicast group address, etc.

To differentiate the TRILL protocol from layer 3 IGP protocols, a new TRILL Protocol-ID = TBD1 is specified.

ESADI (End Station Address Distribution Information) protocol [RFC7357] is a per data label control plane MAC learning solution. MAC address reachability information is carried in ESADI packets. Compared with data plane MAC learning solution, ESADI protocol has security and fast update advantage that are pointed out in [RFC7357].

For an RBridge that is announcing participation in ESADI, the RBridge can distribute MAC address reachability information to external components using BGP. A new "MAC Reachability NLRI" NLRI type TBD2 is used for the MAC address reachability distribution.

The MAC Reachability NLRI uses the format as shown in the following Figure.

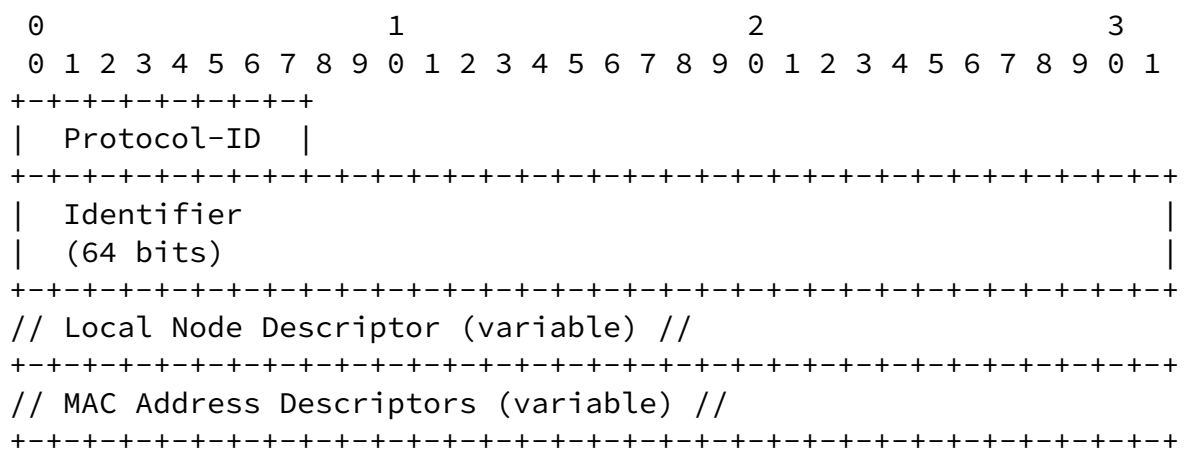


Figure 2: The MAC Reachability NLRI format

3.1 Node Descriptors

The Node Descriptor Sub-TLV types include Autonomous System and BGP-LS Identifier, IS-IS Area-ID and IGP Router-ID. TRILL uses a fixed zero Area Address as specified in [RFC6325], Section 4.2.3. This is

encoded in a 4-byte Area Address TLV (TLV #1) as follows:

0x01, Area Address Type	(1 byte)
0x02, Length of Value	(1 byte)
0x01, Length of Address	(1 byte)
0x00, zero Area Address	(1 byte)

Figure 3: Area Address TLV

3.1.1 IGP Router-ID

Similar to layer 3 IS-IS, TRILL protocol uses 7-octet "IS-IS ID" as the identity of an RBridge or a pseudonode, IGP Router ID sub-TLV in Node Descriptor TLVs contains the 7-octet "IS-IS ID". In TRILL network, each RBridge has a unique 48-bit (6-octet) IS-IS System ID. This ID may be derived from any of the RBridge's unique MAC addresses or configured. A pseudonode is assigned a 7-octet ID by the DRB (Designated RBridge) that created it, the DRB is similar to the "Designated Intermediate System" (DIS) corresponding to a LAN.

3.2 MAC Address Descriptors

The "MAC Address Descriptor" field is a set of Type/Length/Value (TLV) triplets. "MAC Address Descriptor" TLVs uniquely identify an MAC address reachable by a Node. The following attributes TLVs are defined:

TLV Code Point	Description	Length	Value defined in:
1	MAC-Reachability	variable	section 3.2.1

Table 1: MAC Address Descriptor TLVs

3.2.1 MAC-Reachability TLV

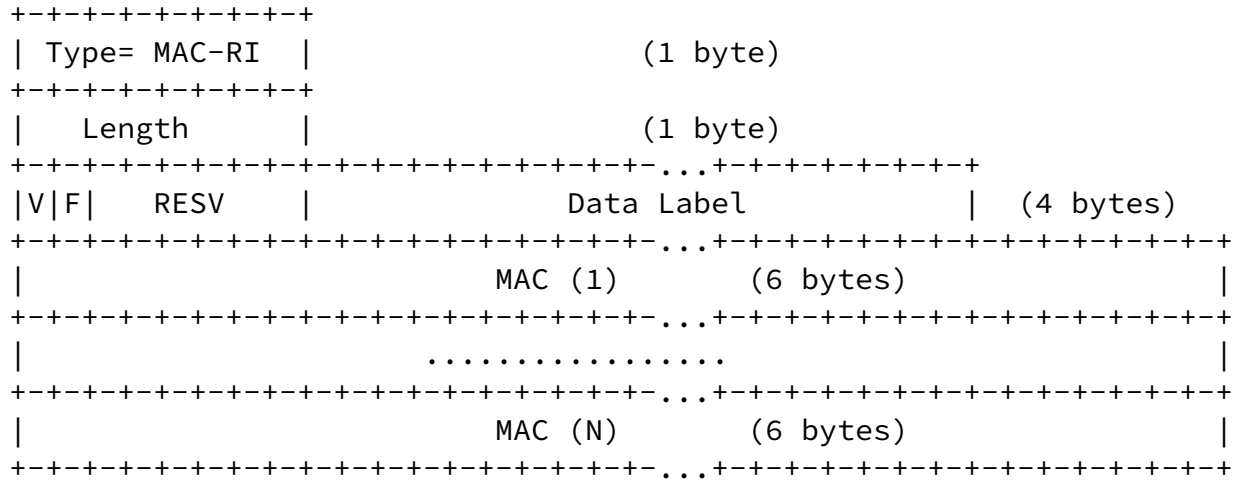


Figure 4: MAC-Reachability TLV format

Length is 4 plus a multiple of 6.

The bits of 'V' and 'F' are used to identify Data Label type and are defined as follows:

Bit	Description
'V'	VLAN
'F'	Fine Grained Label

Table 2: Data Label Type Bits Definitions

Notes: If BGP LS is used for NV03 network MAC address distribution between an external SDN Controller and NVE, Data Label can be used to represent 24 bits VN ID.

3.3 The BGP-LS Attributes

[3.3.1](#) Node Attribute TLVs

[3.3.1.1](#) Node Flag Bits TLV

A new Node Flag bit TBD4 is added to the Node Flag Bits TLV. This flag indicates that the node is a distributed Layer 3 gateway [[RFC7956](#)].

[3.3.1.2](#) Opaque Node Attribute TLV

The Opaque Node Attribute TLV is used as the envelope to transparently carry TRILL specific information. In most cases, this information is encoded as sub-TLVs within the IS-IS Router Capability and MT-Capability TLVs or as the Group Address (GADDR) TLV. Many of these are specified in [[RFC7176](#)] but additional sub-TLVs have been specified and may be specified in the future that also can be carried using the Opaque Node Attribute TLV.

[3.3.2](#). Link Attribute TLVs

Link attribute TLVs are TLVs that may be encoded in the BGP-LS attribute with a link NLRI. Besides the TLVs that has been defined in [[RFC7752](#)] [section 3.3.2](#) Table 9, the following 'Link Attribute' TLV is provided for TRILL.

TLV Code	Description	IS-IS TLV	Defined in:
Point		/Sub-TLV	
TBD3	Link MTU	22/28	[RFC7176] Section 2.4

[4.](#) Operational Considerations

This document does not require any MIB or YANG model to configure operational parameters.

Any implementation of this specification (i.e. that distributes TRILL Link-State information using BGP), MUST do the malformed attribute checks below, and if it detects a malformed attribute, it should use the 'Attribute Discard' action per [[I-D.ietf.idr-error-handling](#) [section 2](#)].

An implementation MUST perform the following expanded BGP-LS syntactic check for determining if the message is malformed:

- o Does the sum of all TLVs found in the BGP LS attribute correspond to the BGP LS path attribute length ?
- o Does the sum of all TLVs found in the BGP MP_REACH_NLRI attribute correspond to the BGP MP_REACH_NLRI length ?
- o Does the sum of all TLVs found in the BGP MP_UNREACH_NLRI attribute correspond to the BGP MP_UNREACH_NLRI length ?
- o Does the sum of all TLVs found in a Node-, Link, prefix (IPv4 or IPv6) NLRI attribute correspond to the Node-, Link- or Prefix Descriptors 'Total NLRI Length' field ?

- o Does every fixed length TLV correspond to the TLV Length field in this document ?
- o Does the sum of MAC reachability TLVs equal the length of the field?

In addition, the following checks need to be made for the fields specific to the BGP LS for TRILL:

PROTOCOL ID is TRILL

NLRI types are valid

MAC Reachability NLRI has correct format including:

- o Identifier (64 bits),
- o local node descriptor with AREA address TLV has the form found in Figure 2

[5](#). Security Considerations

Procedures and protocol extensions defined in this document do not affect the BGP security model. See [[RFC6952](#)] for details.

[6.](#) IANA Considerations

For all of the following assignments, [this document] is the reference.

IANA is requested to assign one Protocol-ID TBD1 for "TRILL" from the BGP-LS registry of Protocol-IDs.

IANA is requested to assign one NLRI Type TBD2 for "MAC Reachability" from the BGP-LS registry of NLRI Types.

IANA is requested to assign one new TLV type TBD3 for "Link MTU" from the BGP-LS registry of BGP-LS Attribute TLVs.

IANA is requested to create a registry for BGP-LS Node Descriptor Flag Bits and to assign one Node Flag bit TBF4 [bit 6 suggested] for "Layer 3 Gateway". This new registry is to be added to the IANA Border Gateway Protocol - Link State (BGP-LS) Parameters web page as follows:

Name: BGP-LS Node Descriptor Flag Bits

Registration Procedure: Expert Review

Reference: [[RFC7752](#)]

Note: These bits are in the payload of the Node Flag Bits TLV.

The bit array is variable length so the maximum bit value assignable is very large; however, length of the TLV grows linearly with the highest numbered flag bit used and there may be practical limits on the length of the TLV.

Bit	Letter	Description	Reference
0	O	Overload Bit	[IS010589]
1	T	Attached Bit	[IS010589]
2	E	External Bit	[RFC2328]
3	B	ABR Bit	[RFC2328]
4	R	Router Bit	[RFC5340]
5	V	V6 Bit	[RFC5340]
6	G	L3 Gateway Bit	[this document] [RFC7956]
7-up	-	Unassigned	

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