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Abstract

This document specifies a multi-message packet format that may be used by mobile ad hoc network routing and other protocols.

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1. Introduction

This document specifies the syntax of a general purpose multi-message packet format for information exchange between MANET routers.

Messages consist of a message header, which is designed for control of message dissemination, and a message body, which contains protocol information. Only the syntax of the message body is specified. All syntactical entities, including messages and packets, are specified using regular expressions.

This document specifies:

- o A packet format, allowing zero or more messages to be contained within a single transmission, and optionally including a packet header.
- o A message format, where a message is composed of a message header and a message body.
- o A message header format containing *all* necessary information to allow a node to make forwarding decisions without inspecting and processing the message body. Message header information permits single- and multi-hop message diffusion.
- o A message body format, containing attributes associated with the message or the originator of the message, as well as blocks of addresses with associated attributes.
- o An address block format, where an address block represents sets of addresses in a compact (compressed) form.
- o A generalized type-length-value (TLV) format representing attributes. Multiple TLVs can be included and associated with a packet, a message, an address, or a set of addresses.

The specification has been explicitly designed with the following properties in mind:

Parsing logic - the regular expression specification facilitates generic, protocol independent, parsing logic.

Extensibility - packets and messages defined by a protocol using this specification are extensible through defining new message types and new TLVs. Full backward compatibility can be maintained.

Efficiency - when reported addresses share common bit sequences (e.g. prefixes or IPv6 interface identifiers) the address block representation allows for a compact representation.

Separation of forwarding and processing - duplicate detection and controlled scope message forwarding decisions can be made solely using information contained in the message header, without processing the message body.

Terminology

The key words "MUST", "MUST NOT", "REQUIRED", "SHALL", "SHALL NOT", "SHOULD", "SHOULD NOT", "RECOMMENDED", "MAY", and "OPTIONAL" in this document are to be interpreted as described in RFC 2119. [1].

Additionally, this document uses the following terminology:

- Packet the top level entity in this specification. Packets are transmitted over a single logical hop and are not forwarded. A packet contains zero or more messages, and may contain a packet header.
- Message the fundamental entity carrying protocol information, in the form of addresses and TLVs. Messages are transmitted in packets, and may be forwarded based on their header information.
- Address an address of the same length as the source IP address in the IP datagram carrying the packet.
- TLV a Type-Length-Value structure. This is a generic way in which an attribute can be represented and correctly parsed, without the parser having to understand the attribute.
- element a syntactic entity defined in the regular expression specification, represented using the notation <foo>.
- <foo> if <foo> is an 8 or 16 bit field then <foo> is also used to represent the value of that field.
- ? zero or one occurrences of the preceding element.
- * zero or more occurrences of the preceding element.
- bar a variable, usually obtained through calculations based on the value(s) of field(s). Variables are introduced into the specification solely as a means to clarify the description.
- address-length a variable whose value is the length of an address in octets, it is 4 if using IPv4, 16 if using IPv6.

3. Applicability Statement

This specification describes a generic multi-message packet format, for carrying MANET routing protocol signals. The specification has been developed from that used by OLSR (The Optimized Link State Routing Protocol) [4].

The specification is designed specifically with IP (IPv4/IPv6) in mind. All addresses within a control message are assumed to be of the same size, deduced from IP. In the case of mixed IPv6 and IPv4 addresses, IPv4 addresses are carried in IPv6 as specified in [2].

The messages defined by this specification are designed to carry routing protocol signals between MANET routers, and to support scope limited diffusion, as well as point to point signaling in a multi-hop network.

The packets defined by this specification are designed to carry a number of messages between in a single transmission. The packets may use any transport mechanism (unicast, multicast) and transport protocol (TCP, UDP, ...) appropriate to the protocol using this specification and may travel over a single logical hop which might consist of one or more IP hops. When the diffusion mechanism enabled by this specification is employed, UDP may be most appropriate.

This specification is particularly appropriate for extensible protocols. It offers external extensibility in the form of new message types. It offers internal extensibility in the form of TLVs, which may be added to existing message types.

A protocol using the multi-message packet format defined by this specification may constrain the syntax (for example requiring a full message header) and features (for example specifying the suggested diffusion mechanism) that it will employ.

4. Protocol Overview and Functioning

This specification does not describe a protocol. It describes a packet format, which may be used by any mobile ad hoc network routing or other protocol.

5. Signaling Framework

This section provides syntactical specification of a packet, represented by the element <packet> and the elements from which it is composed. The specification is given in the form of regular expressions. Illustrations of specified elements are given in Appendix A.

The length of a <packet> is obtained as the size of the payload of the transport protocol employed.

5.1. Packets

where:

- <zero> is an 8 bit field with all bits cleared ('0'). This field
 serves to identify that the packet starts with a packet header.
- <packet-semantics> is an 8 bit field, specifying the composition of
 the packet header:
 - bit 0 (pnoseqnum): if cleared ('0'), then the packet header contains a <packet-seq-number>. If set ('1'), then the packet header does not include a <packet-seq-number>.
 - bit 1 (ptlv): if cleared ('0'), then the packet header does not include a TLV block. If set ('1'), then the packet header includes a TLV block.
 - bits 2-7: are RESERVED, and MUST each be cleared ('0') to be in conformance with this version of the specification.

<packet-seq-number> is omitted if the pnoseqnum bit is set ('1'),
 otherwise is a 16 bit field, specifying a packet sequence number.

<tlv-block> is omitted if the ptlv bit is cleared ('0'), and is otherwise defined in <u>Section 5.3</u>.

Note that since the message type zero is reserved (see <u>Section 7</u>), the presence or absence of a packet header can be determined by inspecting the first octet of the packet.

5.2. Messages

Information is carried through messages. Messages contain:

- o A message header.
- o A message TLV block that contains zero or more TLVs, associated with the whole message.
- o Zero or more address blocks, each containing one or more addresses.
- o A TLV block, containing zero or more TLVs, following each address block.

<message> is defined by:

<message> = <msg-header> <tlv-block>

{<addr-block><tlv-block>}*

<msg-header> = <msg-type>

<msg-semantics>
<msg-size>

<msg-header-info>

<msg-header-info> = <originator-address>?

<hop-limit>?
<hop-count>?
<msg-seq-number>?

where:

<tlv-block> is defined in Section 5.3.

- <addr-block> is defined in Section 5.2.1.
- <msg-type> is an 8 bit field, specifying the type of message. A
 type with all bits cleared ('0') MUST NOT be used.
- <msg-semantics> is an 8 bit field, specifying the interpretation of
 the remainder of the message header:
 - bit 0 (noorig): if cleared ('0'), then <originator-address> and <msg-seq-number> are included in <msg-header-info>. If set ('1'), then <originator-address> and <msg-seq-number> are not included in <msg-header-info>; this reduced message header does not provide for duplicate suppression.
 - bit 1 (nohops): if cleared ('0'), then <hop-limit> and <hop-count> are included in the <msg-header-info>. If set ('1'), then <hop-limit> and <hop-count> are not included in the <msg-header-info>; this reduced message header does not provide for scope-delimited forwarding.
 - bit 2 (typedep): if cleared ('0'), then the message sequence number in the message is type-independent. If set ('1'), then the message sequence number contained in the message is type dependent (the message originator maintains a sequence number specific to <msg-type>). This bit MUST be cleared ('0') if the noorig bit is set ('1').
 - bits 3-7: are RESERVED and MUST each be cleared ('0') to be in conformance with this version of the specification.
- <msg-size> is a 16 bit field, specifying the size of the <message>,
 counted in octets.
- <originator-address> is an identifier of length equal to addresslength, which serves to uniquely identify the node that originated the message.
- <hop-limit> is an 8 bit field, which contains the maximum number of logical hops a message should be further transmitted.
- <hop-count> is an 8 bit field, which contains the number of logical
 hops a message has traveled.
- <msg-seq-number> is a 16 bit field, which contains a unique number,
 generated by the originator node. The <originator-address>, <msg seq-number>, and, if the typedep bit in the <msg-semantics> field
 is set, the <msg-type> of a message serves to uniquely identify
 the message in the network.

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5.2.1. Address Blocks

An address is specified as a sequence of address-length octets of the form head:mid:tail. An address block is an ordered set of addresses sharing the same head and tail, and having individual mids.

<address block> is defined by:

where:

<num-addr> is an 8 bit field containing the number of addresses
 represented in the address block, which MUST NOT be zero.

<addr-semantics> is an 8 bit field specifying the interpretation of the remainder of the address block:

bit 0 (nohead): if cleared ('0'), then <head-length> is included in <address-block>, and <head> may be included in <address- block>. If set ('1'), then <head-length> and <head> are not included in <address-block>.

bit 1 (notail) and bit 2 (zerotail): MUST NOT both be set ('1'). Otherwise, they are interpreted according to Table 1.

notail	zerotail	<tail-length></tail-length>	++ <tail> ++</tail>
0	0		may be included
0	1	ı included 	not included
1 1	0	not included	'

Table 1

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- bits 3-7: are RESERVED and MUST each be cleared ('0') to be in accordance with this version of the specification.
- <head-length> if present is an 8 bit field, which contains the total
 length (in octets) of the head of all of the addresses.
- head-length is a variable, defined to equal <head-length> if present, or 0 otherwise.
- <head> is omitted if head-length == 0, otherwise it is a field of
 the head-length leftmost octets of all the addresses.
- <tail-length> if present is an 8 bit field, which contains the total length (in octets) of the tail of all of the addresses.
- tail-length is a variable, defined to equal <tail-length> if present, or 0 otherwise.
- <tail> is omitted if tail-length == 0 or if the zerotail bit is set ('1'), otherwise it is a field of the tail-length rightmost octets of all the addresses. If the zerotail bit is set ('1') then the tail-length rightmost octets of all the addresses are all 0.
- mid-length is a variable, which MUST be non-negative, defined by:
 - * mid-length = address-length head-length tail-length
- <mid> is omitted if mid-length == 0, otherwise each <mid> is a field
 of length mid-length octets, representing the mid of the
 corresponding address in the address block.

5.3. TLVs and TLV Blocks

A TLV block is defined by:

where:

<tlv-length> is a 16 bit field, which contains the total length (in octets) of the immediately following <tlv> elements.

<tlv> is defined in <u>Section 5.3.1</u>.

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5.3.1. TLVs

There are three kinds of TLV, each represented by an element <tlv>:

- o A packet TLV, included in a packet header.
- o A message TLV, included in a message before all address blocks.
- o An address block TLV, included in a TLV block following an address block. An address block TLV applies to:
 - * all addresses in the address block; OR
 - * any continuous sequence of addresses in the address block; OR
 - * a single address in the address block.

<tlv> is defined by:

where:

- <tlv-type> is an 8 bit field, specifying the type of the TLV, specific to the TLV kind (i.e. packet- message- or address block TLV).
- <tlv-semantics> is an 8 bit field specifying the interpretation of the remainder of the TLV:
 - bit 0 (extended) and bit 1 (novalue): MUST NOT both be set ('1'). Otherwise, they are interpreted according to Table 2.

+		+		+	++
I	extended	•		<length></length>	<value> </value>
	0		0	8 bits	included
	0		1	 not included	 not included
į	4	İ	0	 	
+	1	+		16 bits +	included ++

Table 2

bit 2 (noindex) and bit 3 (singleindex): MUST NOT both be set ('1'). The former MUST be set ('1') and the latter MUST be cleared ('0') for packet or message TLVs. They are interpreted according to Table 3.

noindex	singleindex	+ <index-start> +</index-start>	<index-stop> </index-stop>
0	0	•	included
0	1	ı included ı	not included
1 1	0	•	

Table 3

bit 4 (multivalue): this bit serves to specify how the <value> field is interpreted, as specified below. This bit MUST be cleared ('0') for packet or message TLVs, if the singleindex bit is set ('1'), or if the novalue bit is set ('1').

bits 5-7: are RESERVED and MUST each be cleared ('0') to be in accordance with this version of the specification.

<index-start> and <index-stop> when present are each an 8 bit field,
 interpreted as follows:

index-start and index-stop are variables, defined according to Table 4. The variable end-index is defined as follows:

- + For message and packet TLVs:
 - end-index = 0

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- + For address block TLVs:
 - end-index = <num-addr> 1

+	+		+	+
•		_		index-stop =
	0			<index-stop> </index-stop>
	0	1	 <index-start></index-start>	
	1	0	 0	end-index
+			+	h

Table 4

For an address block TLV, the TLV applies to the addresses from position index-start to position index-stop (inclusive) in the address block, where the first address has position zero.

number-values is a variable, defined by:

- + number-values = index-stop index-start + 1
- <length> is omitted or is a 8 or 16 bit field according to Table 2.
 If present it MUST NOT be zero. If the multivalue bit is set
 ('1') then <length> MUST be an integral multiple of number-values,
 and the variable single-length is defined by:
 - * single-length = <length> / number-values

- * single-length = <length>
- <value> if present (see Table 2) is a field of length <length>
 octets. In an address block TLV, <value> is associated with the
 addresses from index-start to index-stop, inclusive. If the
 multivalue bit is cleared ('0') then the whole of this field is
 associated with each of the indicated addresses. If the
 multivalue bit is set ('1') then this field is divided equally
 into number-values fields, each of length single-length octets and
 these are associated, in order, with the indicated addresses.

5.3.2. Constraints

TLVs in the same TLV block MUST be sorted in ascending TLV type order.

Two or more TLVs of the same type associated with the same address block MUST NOT both cover any address.

TLVs of the same type associated with the same address block MUST be sorted in ascending index-start order.

5.4. Padding

Packet headers and messages can be padded to ensure 32 bit alignment of each message contained within the packet and of the overall packet length.

All elements are an integer multiple of octets, hence padding can be accomplished by inserting an integer number of <pad-octet> elements after the element that is to be 32 bit aligned.

The number of <pad-octet> elements required to achieve this 32 bit alignment is the smallest number (0 to 3) that, when added to the size of the preceding elements, produces an integer multiple of 4.

<pad-octet> is an 8 bit field with all bits cleared ('0').

There is no need to indicate if padding is included, since a <padoctet> will always precede either a message or the end of the packet. In the former case, the start of a message is indicated by the next non-zero octet parsed.

The padding after a message may be freely changed when a message is forwarded without affecting the message.

6. TLV specification

This document specifies one address block TLV, which is included to allow a standardized way of representing network addresses.

6.1. Address Block TLV Specification

	Name	+ Type +	Length	++ Value
	PREFIX_LENGTH	0 	8 bits 	Indicates that the address is a network address, rather than a host address. The value is the length of the prefix/netmask.

Table 5

An address in an address block without an associated PREFIX_LENGTH TLV may be considered to have a prefix length equal to the address length in bits (i.e. address-length times 8).

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7. IANA Considerations

A new registry for message types must be created with initial assignments as specified in Table 6. Future values in the range 5-127 can be allocated using standards action [3]. Additionally, values in the range 128-255 are reserved for private/local use.

A new registry for packet TLV types must be created, with no initial assignments. Future values in the range 0-127 can be allocated using standards action [3]. Additionally, values in the range 128-255 are reserved for private/local use.

A new registry for message TLV types must be created with no initial assignments. Future values in the range 0-127 can be allocated using standards action [3]. Additionally, values in the range 128-255 are reserved for private/local use.

A new registry for address block TLV types must be created with initial assignments as specified in Table 7. Future values in the range 1-127 can be allocated using standards action [3]. Additionally, values in the range 128-255 are reserved for private/local use.

+	-+
Value Description	
+	- +
0 MUST NOT be allocated.	-
1-4 RESERVED	
+	-+

Table 6

Message type 0 MUST NOT be allocated because a zero-octet signifies a packet header and zero-octets are used for padding. Message types 1 to 4 are reserved because they are used by OLSR [4], which uses a compatible packet/message header format.

Mnemonic	Value Description	İ
,	0 Indicates that associated addresses are network addresses, with given prefix length.	

Table 7

8. Security Considerations

Messages are designed to be carriers of protocol information and MAY, at each hop, be forwarded and/or processed according to the information in the message header by the protocol using this specification. If forwarded messages are unchanged by this protocol, then end-to-end security MAY be implemented, between nodes with an existing security association, by including a suitable message TLV containing a cryptographic signature to the message. Since <hop-count> and <hop-limit> are the only fields that may be modified when such a message is forwarded, this signature can be calculated based on the entire message, including the message header, with the <hop-count> and <hop-limit> fields set to zero ('0') if present.

Packets are designed to carry a number of messages between neighboring nodes in a single transmission and over a single logical hop. Hop-by-hop packet level security MAY be implemented, between nodes with an existing security association, by including a suitable packet TLV containing a cryptographic signature to the packet. Since packets are received as transmitted, this signature can be calculated based on the entire packet, or on parts thereof as appropriate.

9. References

9.1. Normative References

- [1] Bradner, S., "Key words for use in RFCs to Indicate Requirement Levels", RFC 2119, BCP 14, March 1997.
- [2] Hinden, R. and S. Deering, "Internet Protocol Version 6 (IPv6) Addressing Architecture", <u>RFC 3513</u>, April 2003.
- [3] Narten, T. and H. Alvestrand, "Guidelines for Writing an IANA Considerations Section in RFCs", October 1998.

9.2. Informative References

[4] Clausen, T. and P. Jacquet, "The Optimized Link State Routing Protocol", <u>RFC 3626</u>, October 2003.

<u>Appendix A</u>. Illustrations

This informative appendix illustrates the elements, which are normatively specified in $\underline{\text{Section 5}}$ using regular expressions.

Bits labeled Reserved or Resv are cleared ('0'). Bits labeled N and M may be cleared ('0') or set ('1'). Octets labeled Padding are cleared ('0'), and are optional.

Appendix A.1. Packet

Θ	1	L		2		3
0 1 2 3 4	5 6 7 8 9 6	1 2 3 4	5 6 7 8	9 0 1 2	3 4 5 6 7	8 9 0 1
+-+-+-+-+	+-+-+-+-	+-+-+-+	-+-+-+	-+-+-+	-+-+-+-	+-+-+-+
0 0 0 0	0 0 0 Rese	erved 0	0 Pa	cket Seq	uence Num	ber
1				- 1 - 1 - 1 - 1	- 1 - 1 - 1 - 1 -	
 		Message	+ Paddi	ng		
						1
+-+-+-+-+ 	+-+-+-+-	-+-+-+-+	-+-+-+	-+-+-+	-+-+-+-	+-+-+-+
:						:
 +-+-+-+-+-+	+-+-+-	-+-+-+-+	-+-+-+	-+-+-+	-+-+-+-	 +-+-+-+-+
 		Message	+ Paddi	ng		
1						1
+-+-+-+-+	+-+-+-+-	+-+-+-+	-+-+-+	-+-+-+	-+-+-+-	+-+-+-+
0	1	L		2		3
0 1 2 3 4	5 6 7 8 9 6	0 1 2 3 4		9 0 1 2		8 9 0 1
0 1 2 3 4	5 6 7 8 9 6) 1 2 3 4 -+-+-+-+	-+-+-+	9 0 1 2	-+-+-+-	8 9 0 1
0 1 2 3 4 +-+-+-+- 0 0 0 0 0	5 6 7 8 9 6 +-+-+-+- 0 0 0 Rese	- 0 1 2 3 4 -+-+-+-+ erved 0	-+-+-+ 1	9 0 1 2 -+-+-+	-+-+-+- dding	8 9 0 1 +-+-+-
0 1 2 3 4 +-+-+-+- 0 0 0 0 0	5 6 7 8 9 6	- 0 1 2 3 4 -+-+-+-+ erved 0	-+-+-+ 1	9 0 1 2 -+-+-+	-+-+-+- dding	8 9 0 1 +-+-+-
0 1 2 3 4 +-+-+-+- 0 0 0 0 0	5 6 7 8 9 6 +-+-+-+- 0 0 0 Rese	- 0 1 2 3 4 -+-+-+-+ erved 0	-+-+-+ 1	9 0 1 2 -+-+-+	-+-+-+- dding	8 9 0 1 +-+-+-
0 1 2 3 4 +-+-+-+- 0 0 0 0 0	5 6 7 8 9 6 +-+-+-+- 0 0 0 Rese	0 1 2 3 4 -+-+-+-+ erved 0	-+-+-+ 1	9 0 1 2 -+-+-+-+ Pa	-+-+-+- dding	8 9 0 1 +-+-+-
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0 1 2 3 4 +-+-+-+-+ 0 0 0 0 0 +-+-+-+-+-+ 	5 6 7 8 9 6 +-+-+- 0 0 0 Rese	0 1 2 3 4 -+-+-+-+ erved 0 -+-+-+-+	-+-+-+- 1 +-+-+ e + Paddi	9 0 1 2 -+-+-+-+ Pa +-+-+	-+-+-+- dding -+-+-+-	8 9 0 1 +-+-+-+ +-+-+-+
0 1 2 3 4 +-+-+-+-+ 0 0 0 0 0 +-+-+-+-+-+ 	5 6 7 8 9 6 +-+-+- 0 0 0 Rese	0 1 2 3 4 -+-+-+-+ erved 0 -+-+-+-+	-+-+-+- 1 +-+-+ e + Paddi	9 0 1 2 -+-+-+-+ Pa +-+-+	-+-+-+- dding -+-+-+-	8 9 0 1 +-+-+-+ +-+-+-+
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Appendix A.3. Message Body

0	1 2 3
0	1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 7 8 9 0 1
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	Message TLV Block
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	Address Block
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1	Address TLV Block
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+-+	-+-+-+-+-+-+-+-+-+-+-+-+-+

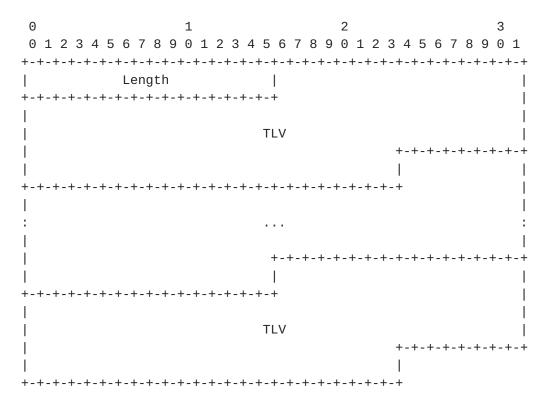
Appendix A.4. Address Block

0 0 1 2 3 4 5 6	1 7 8 9 0 1 2	2 3 4 5 6	2 7 8 9 0 1 2	3 4 5 6	3 7 8 9 0 1
+-+-+-+-+-+-+ Number Addrs +-+-+-+-+-+	Resv	0 0 0	Head Length	H	lead
Head (cont)	Tail L	ength		Tail	1
	Mid	1			
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 		+-+	-+-+-+-	+-+-+-	
+-+-+-+-+	-+-+-+-+	 -+-+-+-	-+-+-+-	Mid +-+-+-	
0 0 1 2 3 4 5 6	1 7 8 9 0 1 2	2 3 4 5 6	2 7 8 9 0 1 2	3 4 5 6	3 7 8 9 0 1
+-+-+-+-+-+ Number Addrs +-+-+-+-+	Resv	0 0 1	Tail Length	1 7	Tail
Tail (cont)	1	Mid		1	
 					į :
1	+-+-+-+	-+-+-+-	-+-+-+-	+-+-+-	
+-+-+-+-+	 -+-+-+	Mid -+-+-+-	-+-+-+-	 +-+	
0 0 1 2 3 4 5 6	1 7 8 9 0 1 2	2 3 4 5 6	2 7 8 9 0 1 2	3 4 5 6	3 7 8 9 0 1
+-+-+-+-+-+ Number Addrs +-+-+-+-+	Resv	0 1 0	Head Length	H	lead
Head (cont)	1	Mid		1	
1					
 	+-+-+-+	-+-+-+-+	-+-+-+-	+-+-+-	 +-+-+-+-+
 	1	Mid		1	

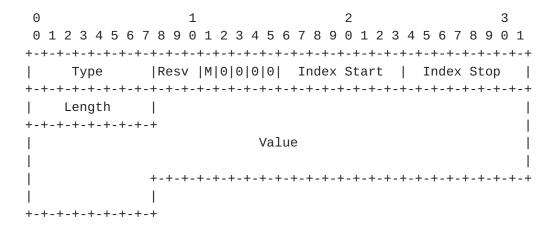
⊙	1 2	3	4 5	5 6	7 8	9 0	1 2	2 3	4	5	6	7	8 9	2	1	2	3 4	4 5	5 6	5 7	8		3 0 1	
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0	1 2	2 3	4 5	5 6	7 8	9 0	1 2	2 3	4	5	6	7	8 9	2	1	2	3 4	4 5	5 6	5 7	8		3 0 1	_
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Appendix A.5. TLV Block



Appendix A.6. TLV



	. 2 3 4 5 6						
I	Type	Resv M	1 0 0 0 1	Index	Start	Index	Stop
l		ength	1	- , - , - , - ,		-,-,-,-,	
 			Val	ue			
 		+-+-+-	+-+-+-+	-+-+-+	-+-+-+	-+-+-+	-+-+-+
+-+-	+-+-+-	+-+					
	. 2 3 4 5 6						
 +-+-	Type		0 0 0 1 0			Index -+-+-+	
	23456			6 7 8 9			
l	Type	Resv M	1 0 1 0 0	Leng	th	-+-+-+	-+-+-+-
+-+- 	+-+-+-+-		Valı	ue		-+-+-+	-+-+-+
+-+-	+-+-+-+-	+-+					
	. 2 3 4 5 6						
I	Type	Resv M	1 0 1 0 1		Leng	th	1
 			Val:	ue			
+-+-	+-+-+-	+-+					

0	1	2	3
0 1 2 3 4 5 6	7 8 9 0 1 2 3 4 5 6	3 7 8 9 0 1 2 3 4 5	6 7 8 9 0 1
+-+-+-+-+-+-		+-+-+-+-+-+-+-+-	+-+-+-+-+-+
	Resv 0 0 1 1 0		
+-+-+-+-+-+-+	+-+-+-+-+-+-+		
0	1	2	3
	7 8 9 0 1 2 3 4 5 6	-	
	+-+-+-+-+-+-+-		
	Resv 0 1 0 0 0		Length
	+-+-+-+-+-	•	0 1
I			
İ	Valu	ıe	į
I			1
1	+-+-+-+-+-+-+-	+-+-+-+-+-+-	+-+-+-+-+-+
1			
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0	4	0	0
0	1	2	3
	7 8 9 0 1 2 3 4 5 6 +-+-+-		
Type	Resv 0 1 0 0 1		Length
	+-+-+-+-+-+-+-		
Length (cont)			1
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1			
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0	1	2	3
-	7890123456	-	
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Type	Resv 0 1 0 1 0		
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Appendix B. Complete Example

An example packet, using IPv4 addresses (length four octets) is shown. This packet has a header, with a packet sequence number but no packet TLV block, and contains a single message. The message has a complete message header, a message TLV block of content length 9 octets containing a single TLV (with the noindex bit of its semantics octet set and value length 6 octets) and then two address blocks each with a following TLV block. The first address block contains 3 addresses, has the notail bit of the its semantics octet set, and has head length 2 octets, hence mid length two octets. It is followed by a TLV block with content length 9 octets containing two TLVs. The first of these TLVs has the noindex bit of its semantics octet set and has a single value of length 2 octets, which applies to all of the addresses in the preceding address block. The second of these TLVs has the novalue bit of its semantics octet set and hence has no length or value fields (it does have index fields, which indicate those addresses this TLV applies to). The second address block contains 2 addresses, has the nohead and zerotail bits of its semantics octet set, and has tail length 2 octets, hence mid length two octets. The two tail octets of each address are zero. It is followed by a TLV block of content length 5 octets. This TLV block contains a single TLV of type PREFIX_LENGTH that has the multivalue and noindex bits of its semantics octet set and a value field length of 2 octets, indicating two values each of one octet length. There is one final padding octet that is not included in the message length of 59 octets.

0 1	2 3
0 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6	6789012345678901
+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-	+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-
0 0 0 0 0 0 0 0 Reserved 0 0	Packet Sequence Number
+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-	+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-
Message Type Resv N 0 0 6	0 0 0 0 0 0 0 0 0 0 1 1 1 0 1 1
+-+-+-+-+-+-	+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+
Originator	Address
+-+-+-+-+-+-	+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-
Hop Limit Hop Count	Message Sequence Number
+-	+-
0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 1	TLV Type Resv 0 0 1 0 0
+-	+-
0 0 0 0 0 1 1 0	Value
+-	+-
Value (cont)	0 0 0 0 0 1 1
+-	+-
0 0 0 0 0 0 1 0 0 0 0 0 0 0 1 0	Head
+-	+-
Mid	Mid
+-	+-
Mid 0	0 0 0 0 0 0 0 0 0 0 0 1 0 0 1
+-	+-
TLV Type Resv 0 0 1 0 0 0	0 0 0 0 0 1 0 Value
+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-	+-
	Resv 0 0 0 1 0 Index Start
+-	
·	0 0 0 0 0 1 0 1 0 0 0 0 0 0 1 0
+-	
Mid	Mid
+-	
0 0 0 0 0 0 0 0 0 0 0 0 1 0 1	
+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-+-	
·	Value1 0 0 0 0 0 0 0 0 0
+-	

Appendix C. Contributors

This specification is the result of the joint efforts of the following contributors from the OLSRv2 Design Team -- listed alphabetically.

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