

IPSECME  
Internet-Draft  
Intended status: Standards Track  
Expires: December 11, 2016

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June 9, 2016

Implicit IV for Counter-based Ciphers in IPsec  
draft-mglt-ipsecme-implicit-iv-00.txt

## Abstract

IPsec ESP sends an initialization vector (IV) or nonce in each packet, adding 8 or 16 octets. Some algorithms such as AES-GCM, AES-CCM, AES-CTR and ChaCha20-Poly1305 require a unique nonce but do not require an unpredictable nonce. When using such algorithms the packet counter value can be used to generate a nonce, saving 8 octets per packet. This document describes how to do this.

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### [1.](#) Requirements notation

The key words "MUST", "MUST NOT", "REQUIRED", "SHALL", "SHALL NOT", "SHOULD", "SHOULD NOT", "RECOMMENDED", "MAY", and "OPTIONAL" in this document are to be interpreted as described in [[RFC2119](#)].

### [2.](#) Introduction

Counter-based AES modes of operation such as AES-CTR ([[RFC3686](#)]), AES-CCM ([[RFC4309](#)]), and AES-GCM ([[RFC4104](#)]) require the specification of a nonce for each ESP packet. The same applies for ChaCha20-Poly1305 ([[RFC7634](#)]). Currently this nonce is sent in each ESP packet ([[RFC4303](#)]). This practice is designated in this document as "explicit nonce".

In some context, such as IoT, it may be preferable to avoid carrying the extra bytes associated to the IV and instead compute it locally on each peer. The local generation of the nonce is designated in this document as "implicit IV".

The size of this nonce depends on the specific algorithm, but all of the algorithms mentioned above take an 8-octet nonce.

This document defines how to compute the nonce locally when it is implicit. It also specifies how to negotiate this behavior within the Internet Key Exchange version 2 (IKEv2 - [\[RFC7296\]](#)).

This document limits its scope to the algorithms mentioned above. Other algorithms with similar properties may later be defined to use this extension.

This document does not consider AES-CBC ([[RFC3602](#)]) as AES-CBC requires the IV to be unpredictable. Deriving it directly from the packet counter as described below is insecure.

### 3. Terminology

- o IoT: Internet of Things
- o IV: Initialization Vector. Although security requirements vary, the common usage of this term implies that the value is unpredictable.
- o Nonce: a fixed-size octet string used only once. This is similar to IV, except that in common usage there is no implication of non-predictability.

#### 4. Implicit IV

With the algorithms listed in [Section 2](#), the 8 byte nonce MUST NOT repeat. The binding between a ESP packet and its nonce is provided using the Sequence Number or the Extended Sequence Number. Figure 1 and Figure 2 represent the IV with a regular 4-byte Sequence Number and with an 8-byte Extended Sequence Number respectively.

[illegible]

Figure 1: Implicit IV with a 4 byte Sequence Number

- o Sequence Number: the 4 byte Sequence Number carried in the ESP packet.
- o Zero: a 4 byte array with all bits set to zero.

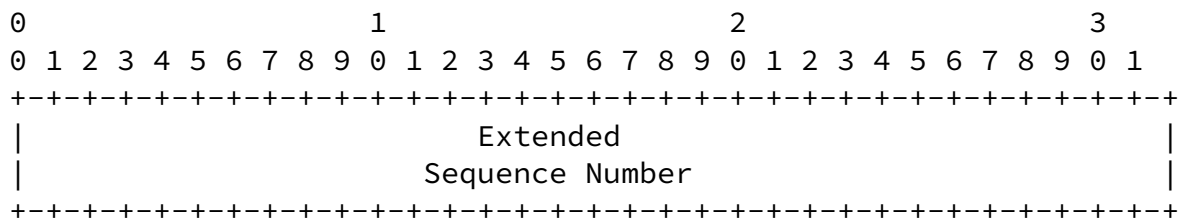


Figure 2: Implicit IV with an 8 byte Extended Sequence Number

- o Extended Sequence Number: the 8 byte Extended Sequence Number of the Security Association. The 4 byte low order bytes are carried in the ESP packet.

## 5. Implicit IV Agreement

NOTE: THIS SECTION SHOWS SEVERAL WAYS TO DO THE SAME THING. OBVIOUSLY THIS DOCUMENT WILL NOT BE PUBLISHED LIKE THAT. WE EXPECT WG DISCUSSION TO PARE THIS DOWN TO JUST ONE WAY OF NEGOTIATING THE USE OF IMPLICIT IV (IIV).

Negotiation of the use of implicit IV can be done in 3 different ways:

- o An IMPLICIT IV Transform Type. A proposal that contains this transform type requires (if accepted) that IPsec use the implicit IV and not include an explicit IV in the packets. To facilitate backward compatibility with non-supporting implementations the Initiator SHOULD add another proposal that does not include this transform type as well as cryptographic suite the Initiator does

not support the implicit IV.

- o An IMPLICIT IV Transform ID. This doubles the number of ENCR transform IDs by creating an ENCR\_AES\_CCM\_16\_IIV for each ENCR\_AES\_CCM\_16.
- o An IMPLICIT IV Transform Attribute, which would be associated to a Transform Type ID, specifying the use of an implicit IV. marks a particular ENCR transform as using implicit IVs. To facilitate backward compatibility with non-supporting implementations the Initiator SHOULD add another ENCR transform for each algorithm so marked. In other words, for each ENCR\_AES\_CCM\_16 with keylength=256 and IIV=1, there will need to be an ENCR\_AES\_CCM\_16 with keylength=256 and no IIV attribute.

## [6.](#) Initiator Behavior

An initiator supporting this feature SHOULD propose implicit IV for all relevant algorithms. To facilitate backward compatibility with non-supporting peers the initiator SHOULD also include those same algorithms without IIV. Depending on the method chosen in [Section 5](#) this may require extra proposals or extra transforms.

## [7.](#) Responder Behavior

An responder supporting this feature SHOULD accept implicit IV for all relevant algorithms. It MUST NOT accept implicit IV for algorithms not specified to be safe for IIV in this or subsequent documents. It SHOULD accept non-IIV proposals for all algorithms if IIV proposals were not included.

The rules of SA payload processing ensure that the responder will never send an SA payload containing the IIV indicator to an initiator that does not support IIV.

## [8.](#) Security Consideration

Nonce generation for these algorithms has not been explicitly defined. It has been left to the implementation as long as certain security requirements are met. This document provides an explicit and normative way to generate IVs. The mechanism described in this document meets the IV security requirements of all relevant algorithms.

## 9. IANA Considerations

TBD: The content of this section depends on our decisions about [Section 5](#).

The following Transform Type is requested: IMPLICIT\_IV Transform Type

Values associated to IMPLICIT Transform Type are:

False: 0 ==> Do we really need a negative value???

True: 1

## 10. Normative References

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## [Appendix A](#). Document Change Log

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Expires December 11, 2016

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June 2016

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