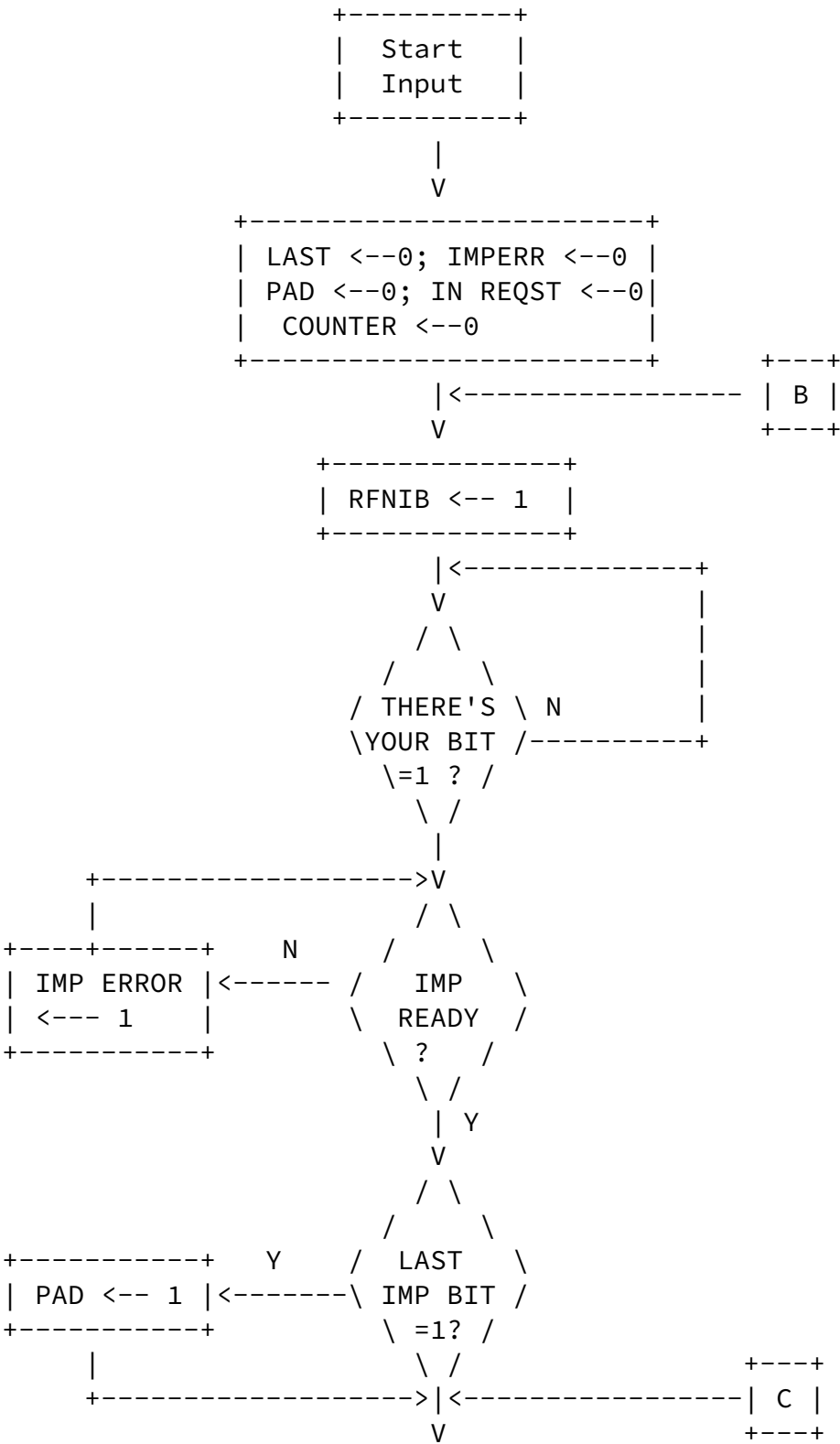
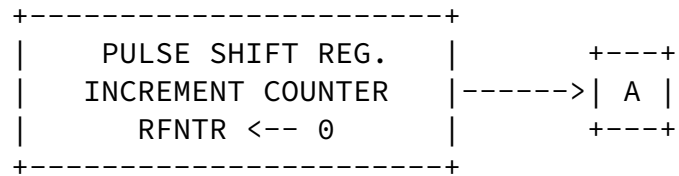


## IMP-HOST INTERFACE FLOW DIAGRAMS

The following flow diagrams were extracted from the logic diagrams provided in [Appendix B](#) of BBN Report No. 1822. These diagrams indicate the logical sequence of hardware operations which occur within the IMP-HOST interface. The logic names appearing in the blocks correspond to the logic elements found in [Appendix B](#).

IMP to HOST Message





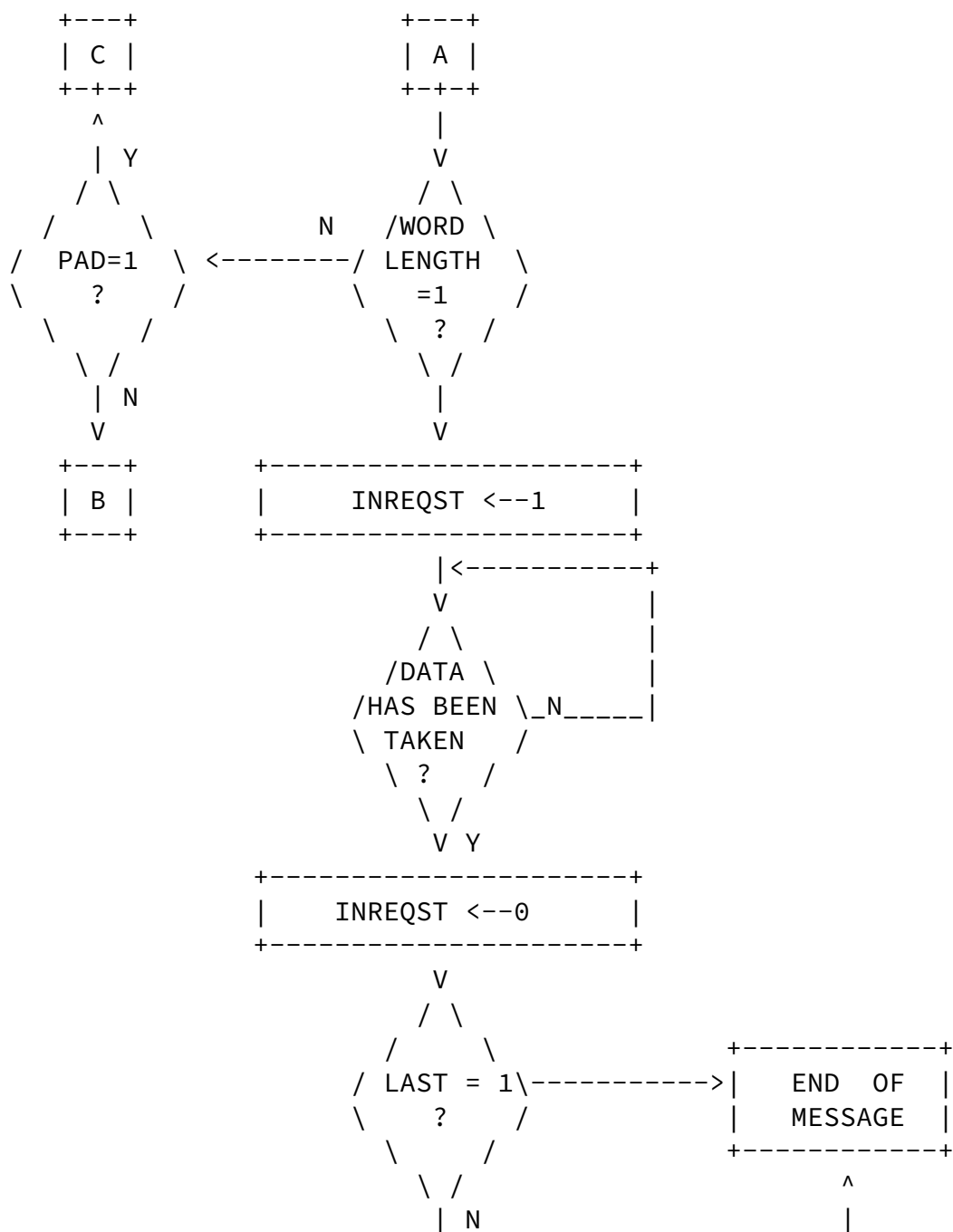
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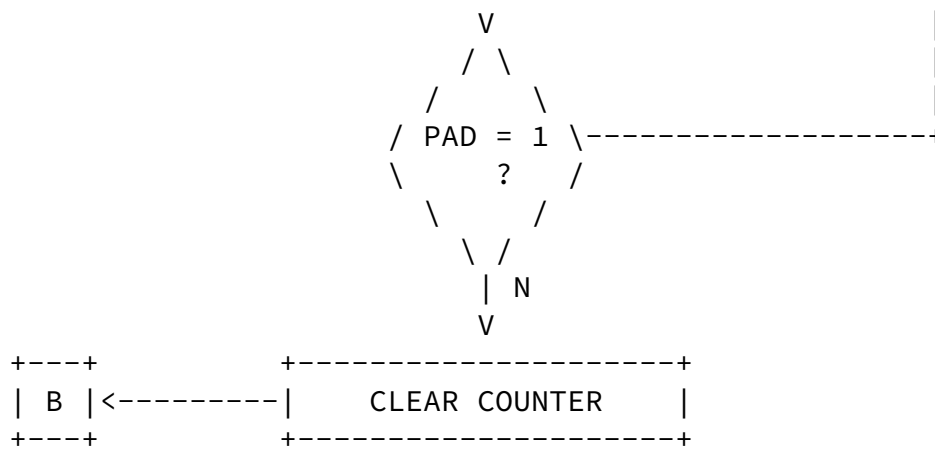
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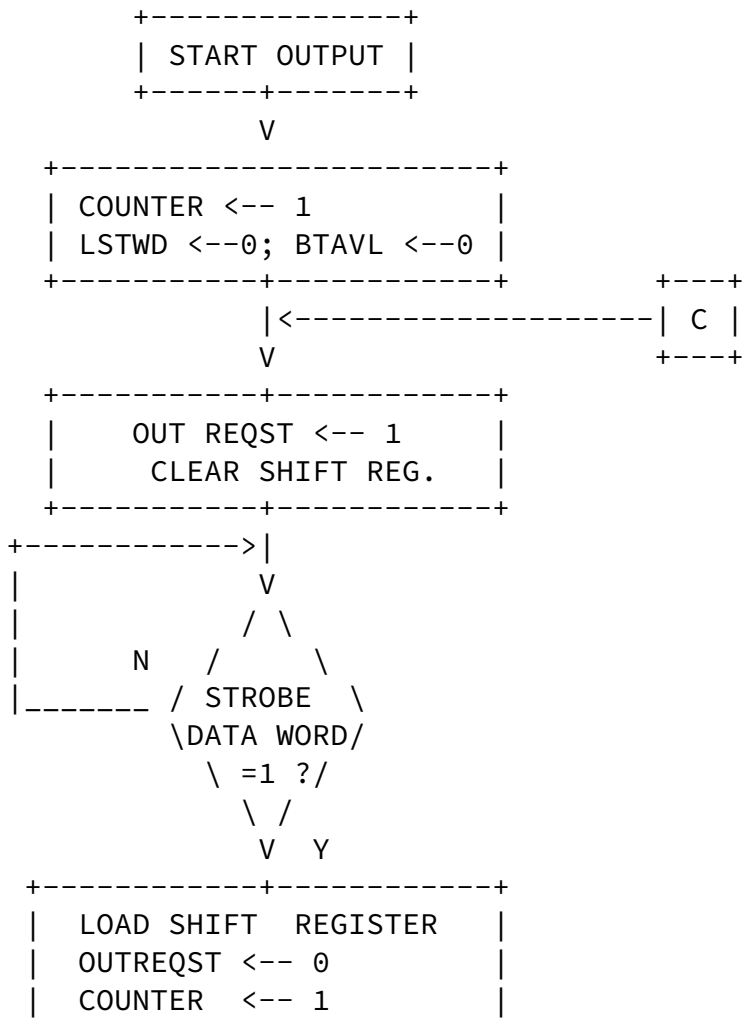
IMP-HOST INTERFACE FLOW DIAGRAMS

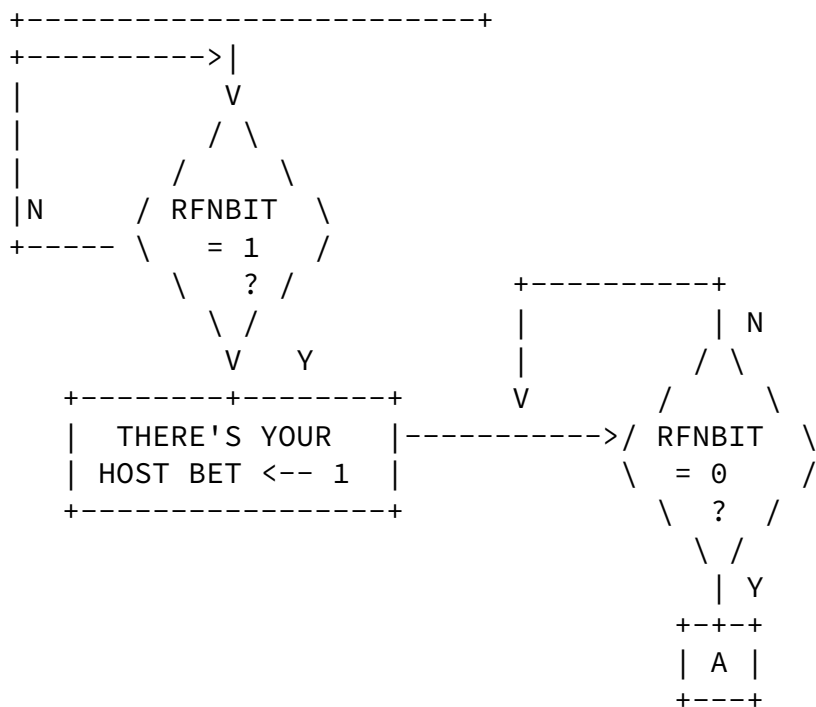
26 August 1969





HOST TO IMP MESSAGE





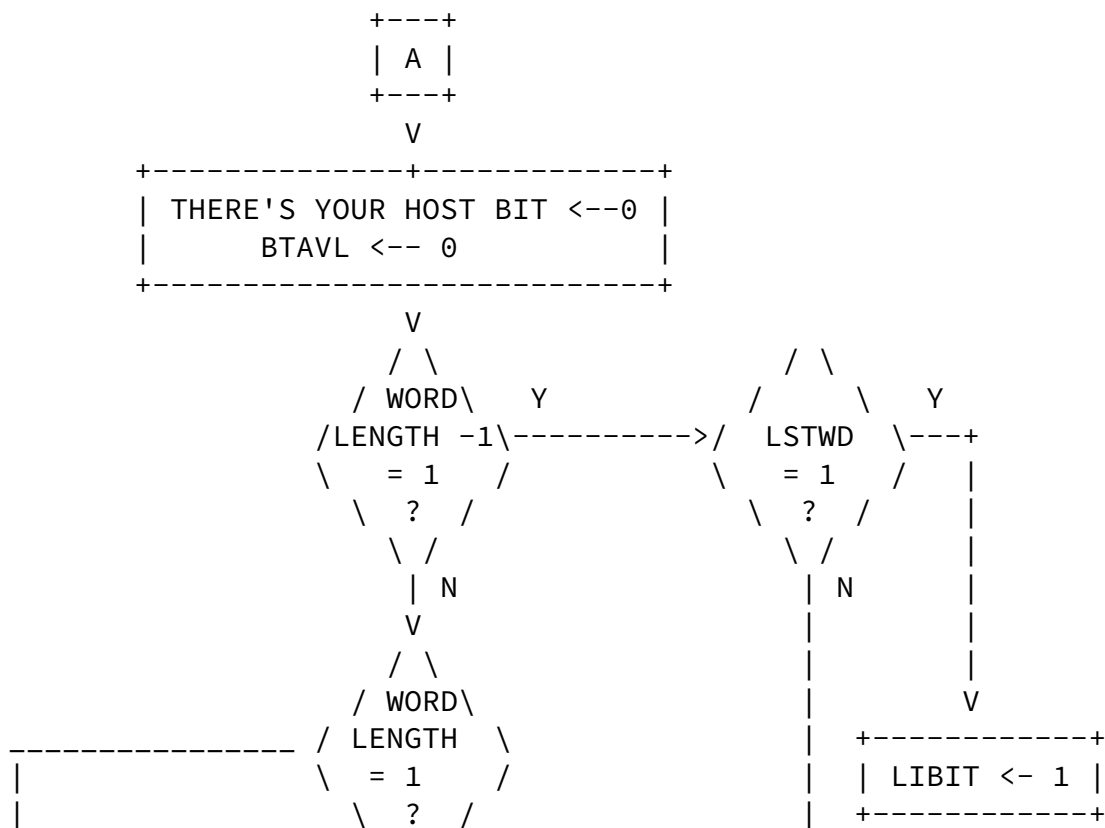
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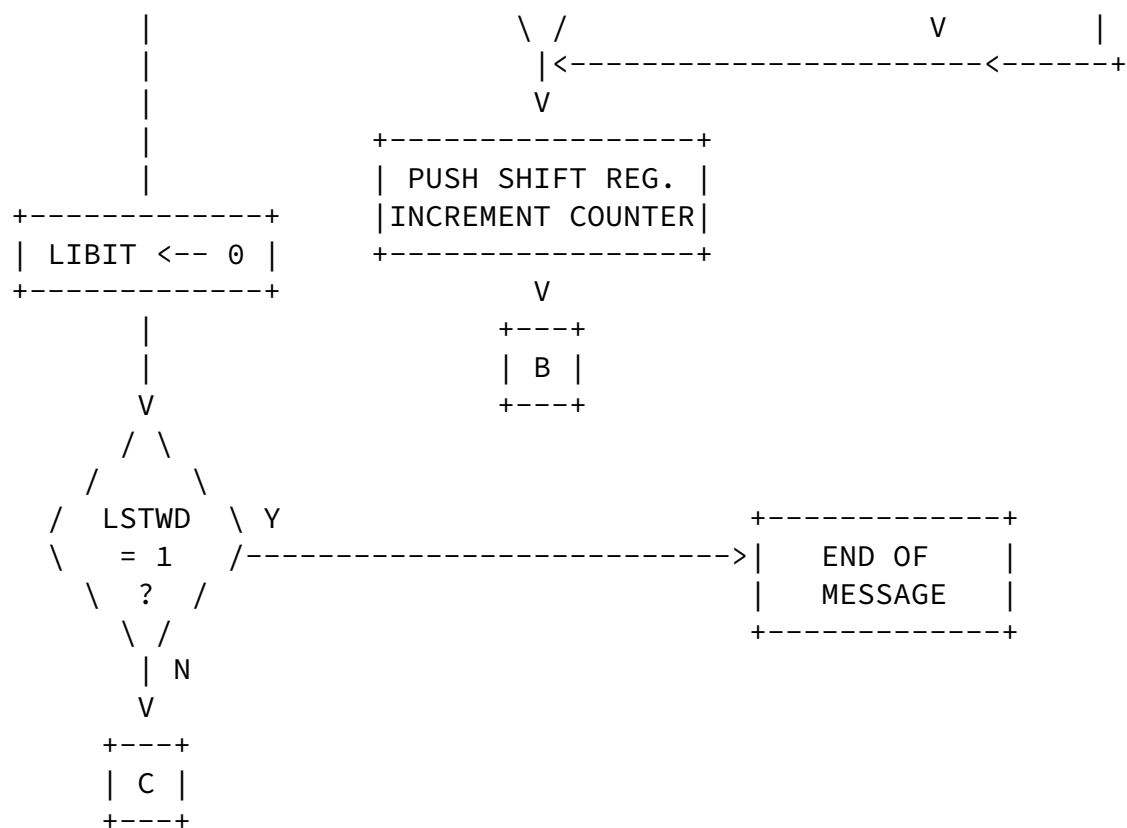
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IMP-HOST INTERFACE FLOW DIAGRAMS

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