

draft-venaas-pim-reserved- bits-00

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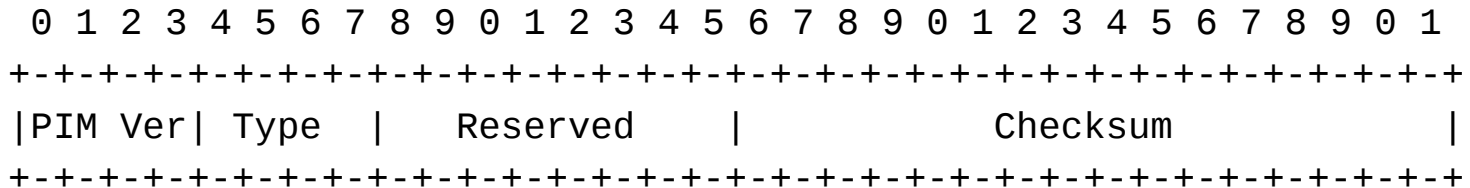
Use of reserved bits

- Several pim message types use reserved bits
 - Not clear that reserved bits should be per message type
 - RFCs using reserved bits should have updated RFC 7761
- This draft fixes the above and defines a registry as below.
 - This only shows what is defined. For type 15, see next slide.

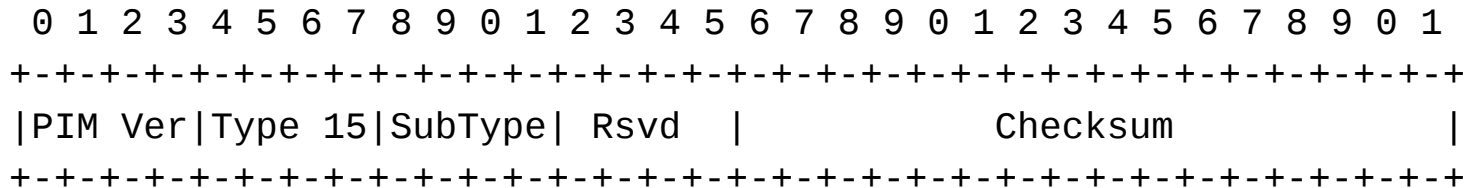
Type	bit(s)	Name	Reference
4	7	No-Forward	[RFC5059]
10	4-7	Sub-type	[RFC5015]
12	7	No-Forward	[draft-ietf-pim-source-discovery-bsr]
15	4-7	Extended type	[this document]

Extending the type space

- The current pim type space is only 4 bits. We have used 0-12.
- The current pim message header is:



- Extend the type space by defining type 15 using 4 reserved bits to define a sub-type (similar to pim DF sub-types).
 - This gives us 16 additional pim message types denoted 15.0 – 15.15.
 - The header for type 15 is defined as below.



- Should we make the sub-type 8 bits?