

PCEP Extensions for SRv6

From
draft-ietf-pce-segment-routing-ipv6-00
to
draft-ietf-pce-segment-routing-ipv6-07

Presenter: Cheng Li

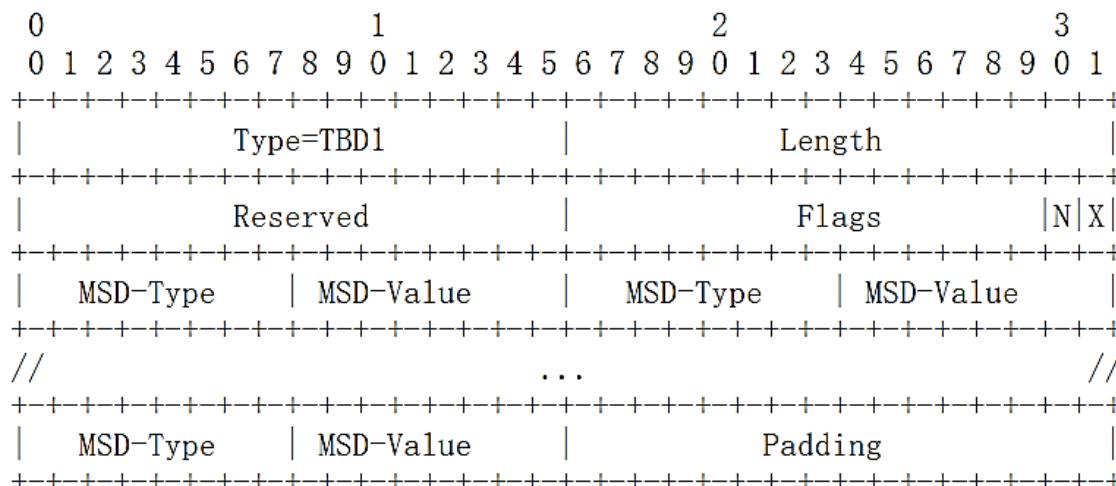
Cheng Li/Mahendra Singh Negi/Mike Koldychev/Prejeeth Kaladharan/Yongqing Zhu

IETF#109

Overview(00-06)

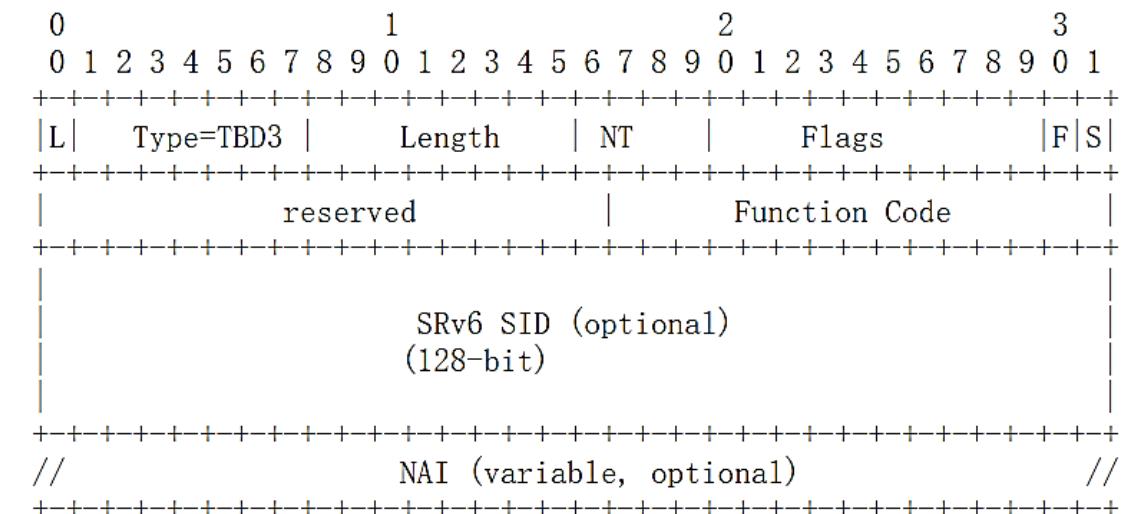
This document mainly defines the following extensions for SRv6 in PCEP.

- Open Object
 - PATH-SETUP-TYPE-CAPABILITY TLV: Adding a new PST=SRv6
 - SRv6 PCE Capability sub-TLV
- New PST for SRv6 in RP/SRP object
- SRv6-ERO Subobject
 - Renamed NAI-Type (NT) (from SID-Type (ST))
- SRv6-RRO Subobject
- It also defines the processing including error handling of the extensions.



SRv6-PCE-CAPABILITY sub-TLV

PCE WG @ IETF 109

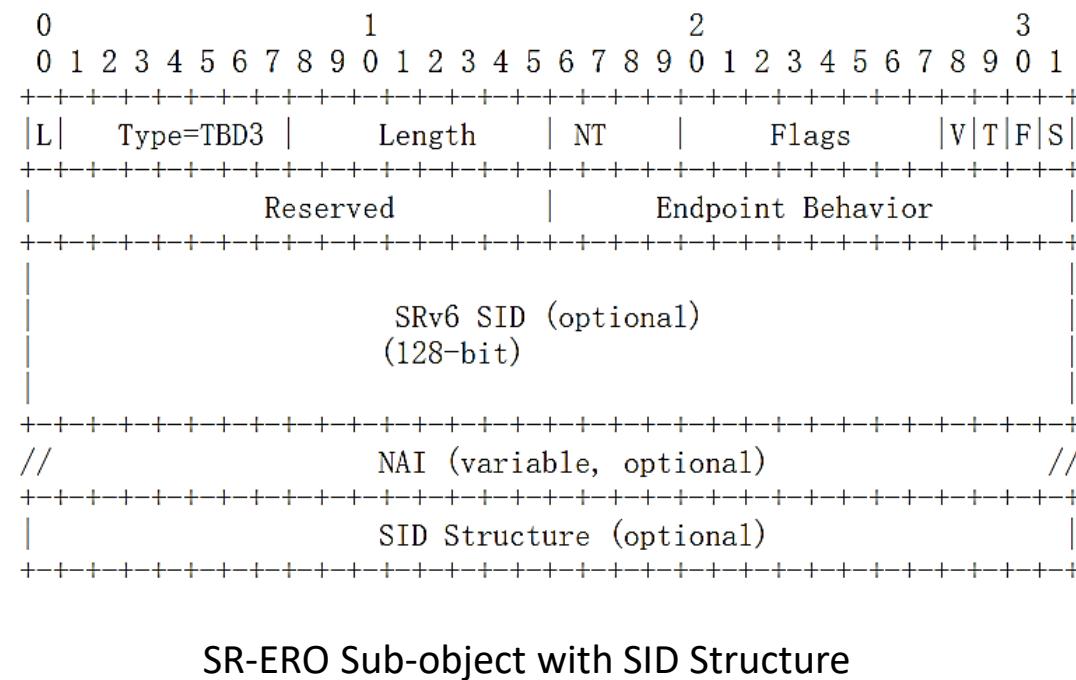


SRv6-ERO Sub-object

Update from 06 to 07

Adding SID Structure TLV to align with the extensions in IS-IS, BGP-LS and BGP SR policy.

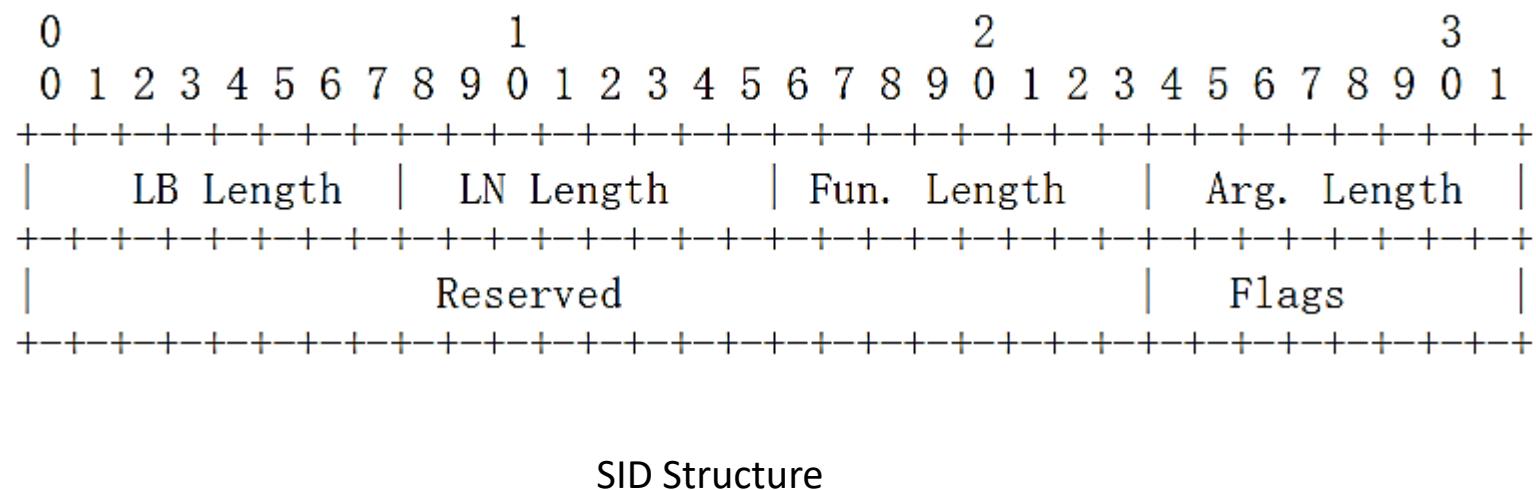
- T: The T bit indicates the presence of an optional 8-byte SID Structure when SRv6 SID is included.
- V: The "SID verification" bit usage is as per Section 5.1 of [[I-D.ietf-spring-segment-routing-policy](#)].
- Do we need the A-flag (Algorithm flag) ?



Update from 06 to 07: SID Structure

SID Structure

- Indicating the length of the four parts of an SRv6 SID.
- 8-bit flag for future usage.
- 24-bits reserved for future usage, like defining extra parts of an SRv6 SID.



Next Step

- Comments are welcome!
- Refine document to specify the details.

Thank you
