Forwarding Actions Indicator draft-kompella-mpls-mspl4fa-03 <u>Kompella</u>, Beeram, Saad, Meilik, Li IETF 114 Philadelphia

New Author

- Tony Li
- Active in the MPLS Open Design Team
 - Author of other drafts related to MNA
- Mea culpa for not updating the author list

Draft Getting Close Technically

- Many discussions on implementability, parsing, skipping FAI block, ...
 - Within Broadcom team (on BCOM chips)
 - Within Juniper team (on JNPR chips)
 - Across teams (on compromises)
- Reasonable confidence in an efficient implementation on a variety of platforms

Big Change: length fields

- Earlier, the extent of a field was indicated by continuation bits (cb)
- This was hard to parse, to parallelize, to predict
- Having a length field is almost a wash in terms of bits needed
 - cb win in case the FAI block is short
 - cb don't impose an a priori length limit on blocks
 - Length fields burn a constant number of bits
 - Length fields allow for easier processing
- There are length fields for the overall FAI block, for standard ISD and for user-defined ISD

Readability Improvement

- Separated description of LSE format and flag definitions
- Better phraseology

Next Steps

- Draft is ready for WG adoption
- Request early allocation of bSPL
- Will work on prototype implementation on multiple PFE types

NFFRR draft-kompella-mpls-nffrr-03 <u>Kompella</u>, Lin

Spurt of Activity Early On

- Good discussion
- Feedback incorporated, references updated
- More details added
- Bogged down with the allocation of bSPL
- bSPL allocation <u>no longer</u> part of this draft bSPL will be allocated as part of the MNA work (FAI or other solutions)

Problem Acknowledged, Solution Acceptable

- No alternatives proposed to the general problem
- <u>The draft is ready for WG adoption</u> (as per authors)
- Will consider prototype as part of prototyping FAI