

PCEP Extensions for SRv6

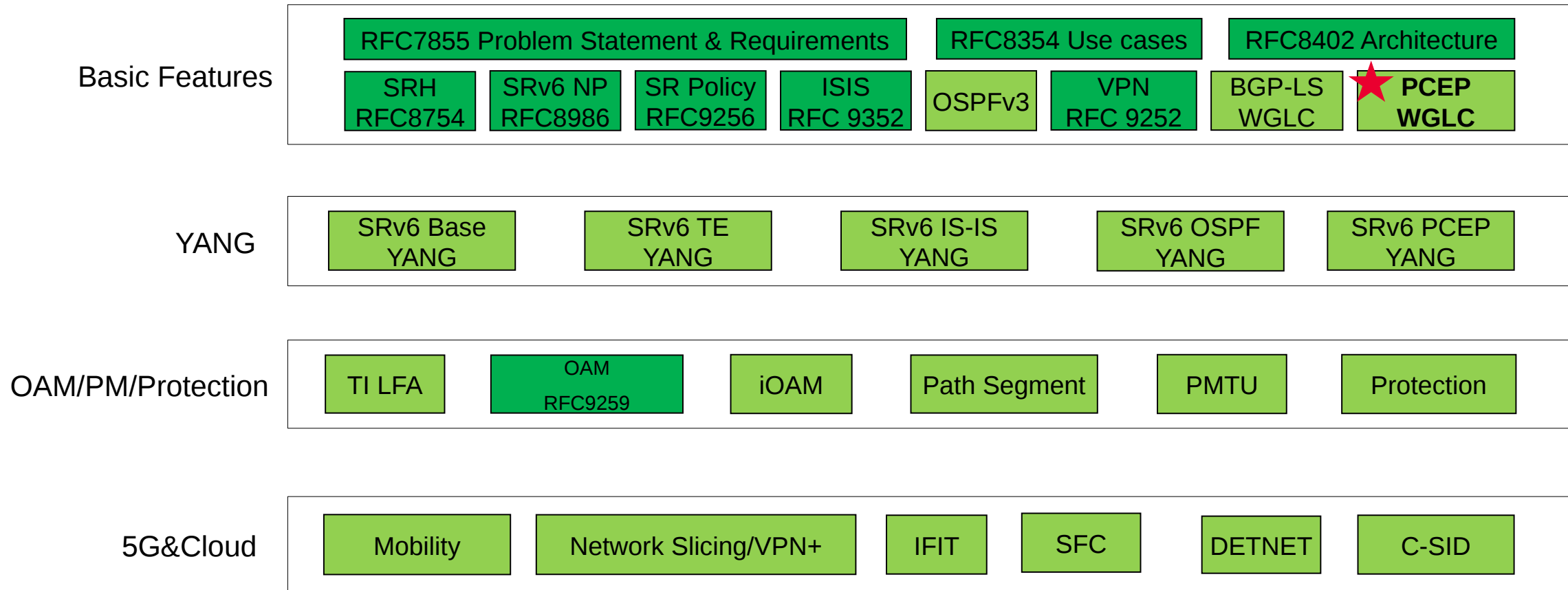
From
draft-ietf-pce-segment-routing-ipv6-16
to
draft-ietf-pce-segment-routing-ipv6-17

Presenter: Li Cheng

Li Cheng/Mahendra Singh Negi/Siva Sivabalan/Mike Koldychev/Prejeeth Kaladharan/Yongqing Zhu

IETF#117

SRv6 protocol extensions: Mature for deployment



New Milestone

All SRv6 basic features have been stable and ready for commercial deployment.

Current Situation: WGLC Comments to be addressed

Versions:

000102030405060708091011121314151617

draft-negi-pce-segment-routing-ipv60001020304

draft-ietf-pce-segment-routing-ipv600020304060708091011121314151617

Oct 2017

Mar 2018

Jun 2018

Oct 2018

Feb 2019

Mar 2019

Apr 2019

Oct 2019

Mar 2020

Jun 2020

Nov 2020

May 2021

Nov 2021

Jan 2022

Mar 2022

Apr 2022

Jul 2022

Oct 2022

Mar 2023

Jun 2023

Document

Type

Active Internet-Draft (pce WG)

Authors

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Last updated

2023-06-27

Replaces

[draft-negi-pce-segment-routing-ipv6](#)

RFC stream

Internet Engineering Task Force (IETF)

Intended RFC status

(None)

Formats

txt

html

xml

htmlized

pdf

bibtex

bibxml

Reviews

[RTGDIR Early review \(of -16\) by Yingzhen Qu](#)

Has Issues

Additional resources

[Mailing list discussion](#)

Stream

[WG state](#)

Waiting for WG Chair Go-Ahead

Revised I-D Needed - Issue raised by WGLC, Doc Shepherd Follow-up Underway

On agenda

Edit

☐ pce at IETF-117

Document shepherd

[Hariharan Ananthakrishnan](#)

- The WGLC ended in March.
- We have addressed the editorial comments received from Quan Xiong, Chongfeng Xie, Adrian Farrel, Ketan Talaulikar and others. Many thanks for your comments!

Comments to be addressed: is X bit needed?

Last Update:

- Add Chen Ran as a new contributor, thank you!
- Addressed Adrian's comments
 - Record Route Object -> Reported Route Object
- Addressed Yingzhen's IESG review comments
- Addressed Ketan's editorial comments
- References update and removal.

☐ Clear current search query, filters, and sorts

☐ 2 Open ✓ 3 Closed

☐  Update draft-ietf-pce-segment-routing-ipv6.md 
#5 by muzixing was closed now • Review required

☐  Add new contributor
#4 by Ranchen666 was closed 2 minutes ago

☐  Adrian's comments
#3 by muzixing was closed 1 minute ago

☐  Yinzhen's Comments
#2 opened on Apr 11 by muzixing

☐  Ketan's comments to be addressed.
#1 opened on Apr 11 by muzixing

X-Flag: is needed or not? Implementation status?

- X bit: A PCC sets this bit to 1 to indicate that it does not impose any limit on MSD (irrespective of the MSD-Type).
- Why X flag is needed?
 - X-flag is used to indicate that it does not impose any limit on MSD
- Why X flag is not needed?
 - A PCC can simply advertise the value of 255 if it is capable instead of the X bit. Another alternative is for PCE implementations to provide a knob to overrule whatever MSD is signaled by the PCC (not sure if this is even a good idea). Do any of the current PCC implementations set this X flag and if so, why? (From Ketan)
- If we do not need it, how to handle this? Ask for implementation of this before making a decision?

Next Step

- Address functional comments from Ketan. Would like to hear wider discussion of these functional comments, since we are in WGLC stage.
- Move the draft to the next stage, accelerate for publication.

Thank you

For your long-term support