

Optimizations of Label Switched Path State Synchronization Procedures for a Stateful PCE

PCE WG, IETF 89th, London, UK

draft-minei-pce-stateful-sync-optimizations-02.txt

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Changes from Version 00

- Merge Section 3.3.1 and 3.3.2 (Use of the LSP-DB-VERSION TLV in the OPEN and LSP object respectively) into Section 3.2 (procedure discussion)
 - ✓ So the procedure related description is provided all together.
- Addressed the comments provided by Jonathan Hardwick on Section 5 (previously on draft-zhx-pce-stateful-lsp-sync-00)
 - ✓ Specified when incremental synchronization is preferred;
 - ✓ Clarified the caching the PCCs needs to perform;
 - ✓ Clarified when the local LSP DB information is not enough to perform incremental LSP synchronization;
- Added Two Error values;
- Editorial check through the document;

Changes from Version 01

- Updated on Monday, addressing comments from Jonathan Hardwick
 - Corrected an error in Figure 2;
 - Further clarified how to deal with the case when incremental LSP synchronization cannot be performed;

Next Step

- This draft is mature enough
 - Two functions from the WG doc ([draft-ietf-pce-stateful-pce](#))
 - The other function well received in the WG;
 - All functions have been implemented;
 - All comments received so far has been addressed;
- WG adoption?