

Optimizations of Label Switched Path State Synchronization Procedures for a Stateful PCE

PCE WG, IETF 90th, Toronto, Canada

draft-ietf-pce-stateful-sync-optimizations-01.txt

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What happened since last IETF?

- draft-minei-pce-stateful-sync-optimizations-02 was adopted as WG shortly after IETF89
- 01 version was posted on **27th, June, 2014**
- No comments received so far

Changes from Version 00

- Restructured the three main sections into four sections, so as to make the description functionally clear;
 - Addressing the only remaining open point Jon raised up during/after last IETF;
 - Most of changes associated with this update is editorial and re-organizing
- Updated the IANA section using "TBD+ suggested value"
 - Per Chairs' request;
- Added explanation texts on how to handle PCE IP address change.

Next Step

- This draft is mature enough
 - No remaining issues;
 - Implementation(s) following this draft;
- The authors believe the document is stable and ready for WG Last Call
 - Moved along with applicability and base draft for stateful PCE;
- Working group
 - Please review and comment, so we can move this document forward;