draft-ietf-6lo-dispatch-iana-registry-03

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IETF95, July 18, 2016

Background

- ITU-T G3-PLC reported their use of ESC bytes dispatch header space along with Mesh Header
  - Deployment of their use of ESC bytes
    - ESC byte Dispatch values 1-31 for command frames [No IANA assignments]
  - Thierry Lys – April, 2015

- Liaison Statement sent to ITU-T SG15
  - https://datatracker.ietf.org/liaison/1425/
  - During IETF 93, July 2015

Update since IETF95

- New Shepherd: Brian Haberman

- Shepherd's comments:

  – Change Title to reflect the document content

  OLD: IANA Registry for 6lowpan ESC Dispatch Code points

  NEW: 6lo ESC Dispatch Code Points and Guidelines

  – Update abstract to say that the document updates RFC4944 and 6282

  New Abstract: RFC4944 defines ESC dispatch type for additional dispatch bytes in the 6lowpan header. The value of ESC byte has been updated by RFC6282. However, the usage of ESC extension byte has not been defined in RFC6282 and RFC4944. This document updates RFC4944 and RFC6282 by defining the ESC extension byte code points including registration of entries for known use-cases at the time of writing of this document

Update since IETF95(2)

– This draft needs to provide guidance to the designated experts on how to assess the specification provided

RFC 5226 section 6.3, 6.4 has guidance. We will add "process of selection of an expert" and "guidance for the expert to assess the dispatch code point validity" in the IANA section [ex: RFC3748-section 6]

– Identify the possible expert contact points in the draft

Proposed Expert Contacts: 6lo Technical Advisor, 6lo WG chairs

Next Step

- Updated draft submission this week
- After that, Ready for IESG submission