

# **Updates to YANG Data Model for IEEE 1588v2**

`draft-ietf-tictoc-1588v2-yang-04`

Y. Jiang, X. Liu, J. Xu, Huawei  
R. Cummings, National Instruments

# **YANG Hierarchy**

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```
module: ietf-ptp-dataset
  +-rw instance-list* [instance-number]
    | +-rw instance-number      uint16
    | + .....
    | +-rw default-ds
    | + .....
    | +-rw current-ds
    | + .....
    | +-rw parent-ds
    | + .....
    | +-rw time-properties-ds
    | + .....
    | +-rw port-ds-list* [port-number]
    | + .....
  +-rw transparent-clock-default-ds
    | + .....
  +-rw transparent-clock-port-ds-list* [port-number]
    + .....
```

# *Summary of Changes in V.04*

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- Updates in draft-ietf-tictoc-1588v2-yang-04
  - ✓ Allow more PTP instances be supported
  - ✓ Reconstruct and simplify the module by introducing several new types
    - ✓ **clock-class, clock-accuracy, clock-identity-type, time-interval-type**, etc
    - ✓ **Introduce several new type of emulations**
  - ✓ Minor Editorial updates...

# ***Open Source***

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- The newest module is uploaded to the github site:

<https://github.com/YangModels/yang/tree/master/standard/ietf/DRAFT>

# **Next Step**

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- Will add a new leaf interface-reference in grouping port-ds-entry, so that any underlining transport port can be referenced properly
- Welcome more WG reviews and comments
- Solicit WG Last Call

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*Thank You*